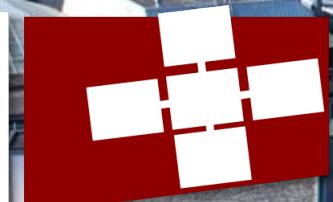


BRYAN A. PLUMMER*, NIKOLI DRYDEN*, JULIUS FROST, TORSTEN HOEFLER, KATE SAENKO

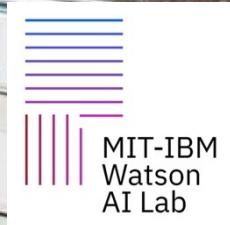
Neural Parameter Allocation Search



BOSTON
UNIVERSITY



This project received funding from DARPA; the National Science Foundation; and the European Research Council under grant agreement MAELSTROM.



BRYAN A. PLUMMER*, NIKOLI DRYDEN*, JULIUS FROST, TORSTEN HOEFLER, KATE SAENKO

Neural Parameter Allocation Search

ICLR 2022

arXiv:2006.10598

NEURAL PARAMETER ALLOCATION SEARCH

Bryan A. Plummer*,†, Nikoli Dryden*,‡, Julius Frost,† Torsten Hoefler,†, Kate Saenko*,§

†Boston University, ‡ETH Zürich, §MIT-IBM Watson AI Lab

{bplum, juliusf, saenko}@bu.edu

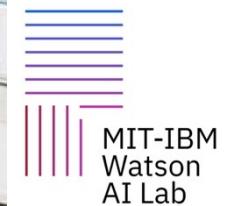
{nikoli.dryden, torsten.hoefler}@inf.ethz.ch

ABSTRACT

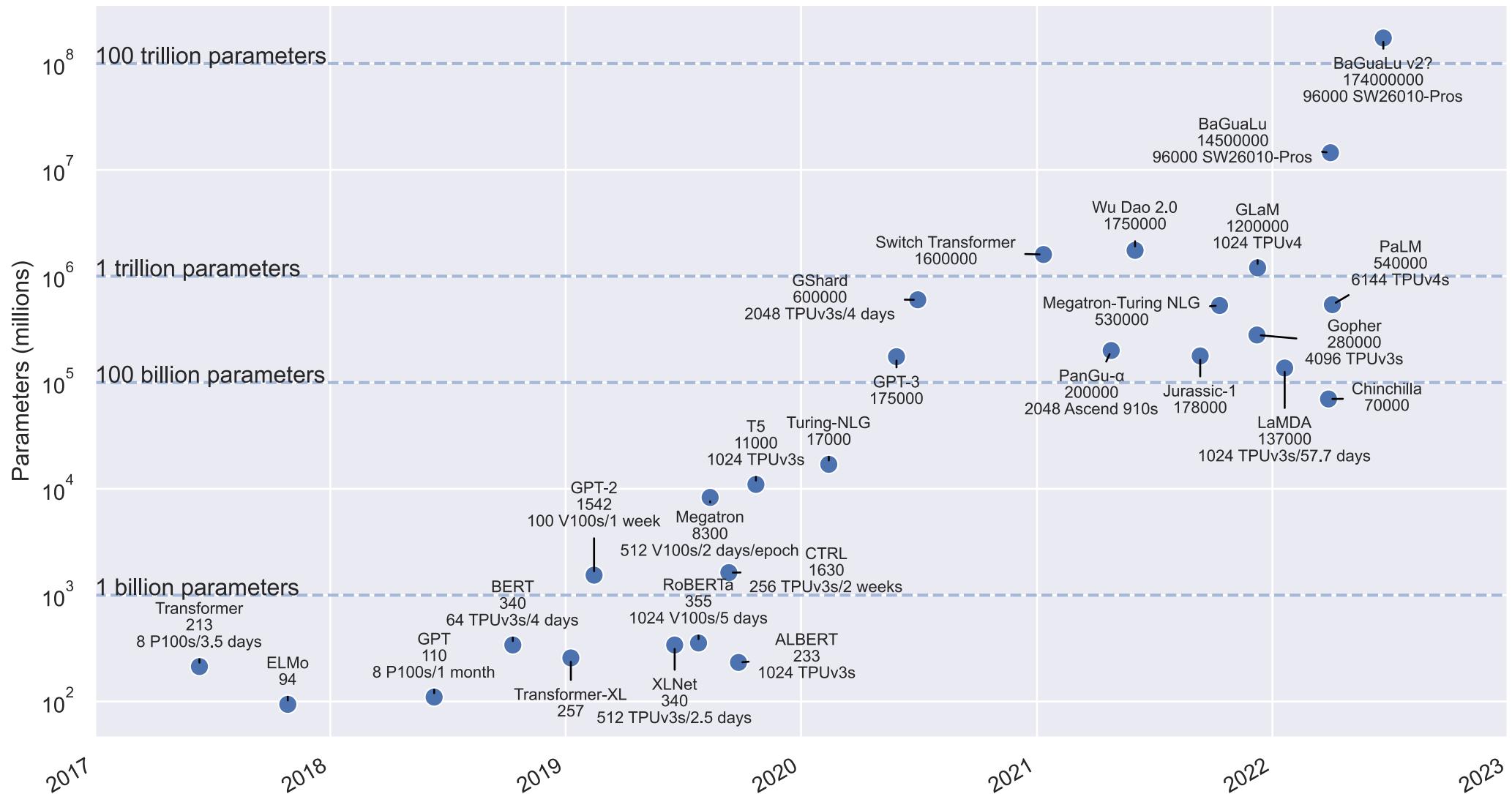
Training neural networks requires increasing amounts of memory. Parameter sharing can reduce memory and communication costs, but existing methods assume networks have many identical layers and utilize hand-crafted sharing strategies that fail to generalize. We introduce Neural Parameter Allocation Search (NPAS), a novel task where the goal is to train a neural network given an arbitrary, fixed parameter budget. NPAS covers both low-budget regimes, which produce compact networks, as well as a novel high-budget regime, where additional capacity can be added to boost performance without increasing inference FLOPs. To address NPAS, we introduce Shapeshifter Networks (SSNs), which automatically learn where and how to share parameters in a network to support any parameter budget without requiring any changes to the architecture or loss function. NPAS and SSNs provide a complete framework for addressing generalized parameter sharing, and can also be combined with prior work for additional performance gains. We demonstrate the effectiveness of our approach using nine network architectures across four diverse tasks, including ImageNet classification and transformers.

BOSTON
UNIVERSITY

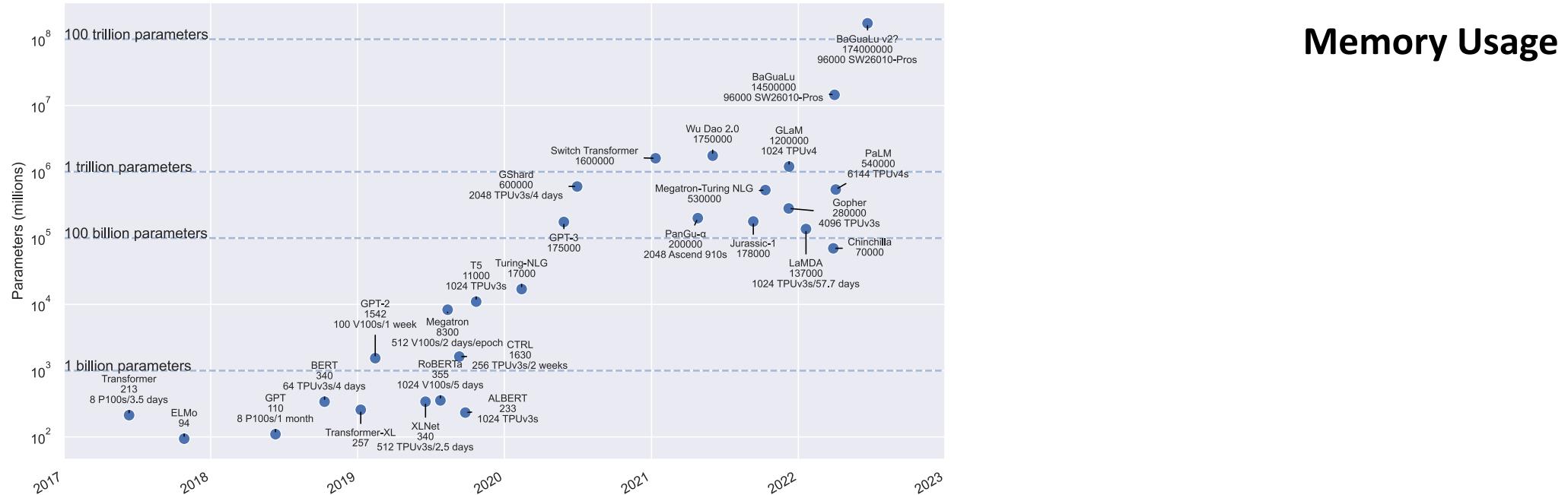
This project received funding from DARPA; the National Science Foundation; and the European Research Council under grant agreement MAELSTROM.



The Memory Explosion



The Memory Explosion



The Memory Explosion



Parameters

Memory Usage

The Memory Explosion



Memory Usage

Parameters

Activations

The Memory Explosion



Memory Usage

Parameters

Activations

Gradients

The Memory Explosion



Memory Usage

Parameters

Activations

Gradients

Optimizer
state

The Memory Explosion



Memory Usage

100 trillion parameters, FP32, Adam

Parameters

Activations

Gradients

Optimizer
state

The Memory Explosion



Memory Usage

100 trillion parameters, FP32, Adam

Parameters

Activations

Gradients

Optimizer state

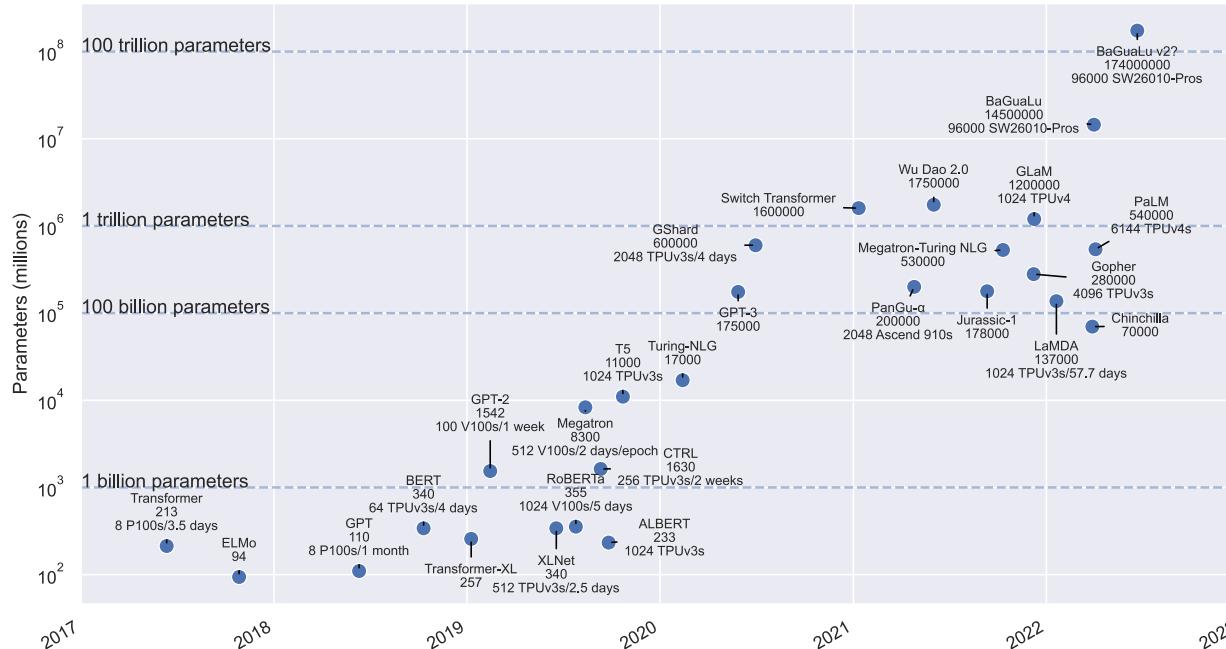
Advanced Computing Ecosystem Request for Information

Version: 1.6

1. Introduction

The US Department of Energy (DOE) has a long history of deploying leading-edge computing capabilities for science and national security. The acquisition plans of the large DOE compute facilities “Notional system architecture ... for large-scale AI training (100 trillion parameter models)”
Traditional facilities take 1-2 years to plan and build, some facilities take up to two years). This request for information (RFI) from computing hardware and software vendors, system integrators, and other entities will assist the DOE national laboratories (labs) to plan, design,

The Memory Explosion



Memory Usage

100 trillion parameters, FP32, Adam

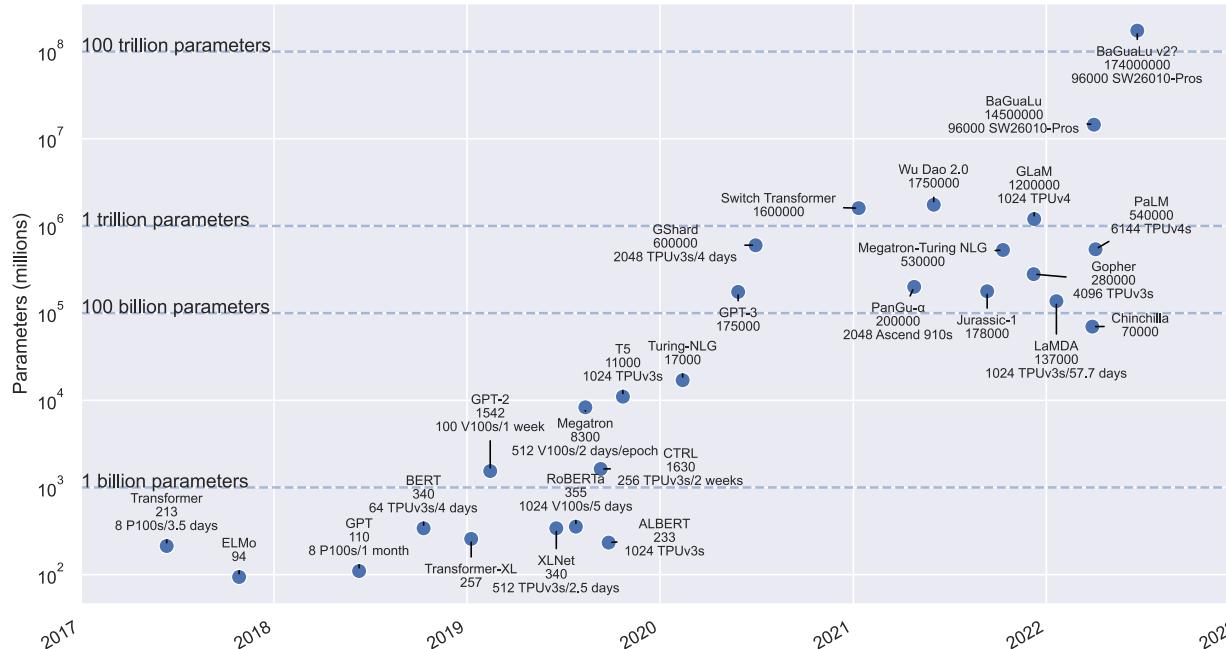
Parameters

Activations

Gradients

Optimizer
state

The Memory Explosion



Memory Usage

100 trillion parameters, FP32, Adam

Parameters

Activations

Gradients

Optimizer
state

400 TB

The Memory Explosion



Memory Usage

100 trillion parameters, FP32, Adam

Parameters

Activations

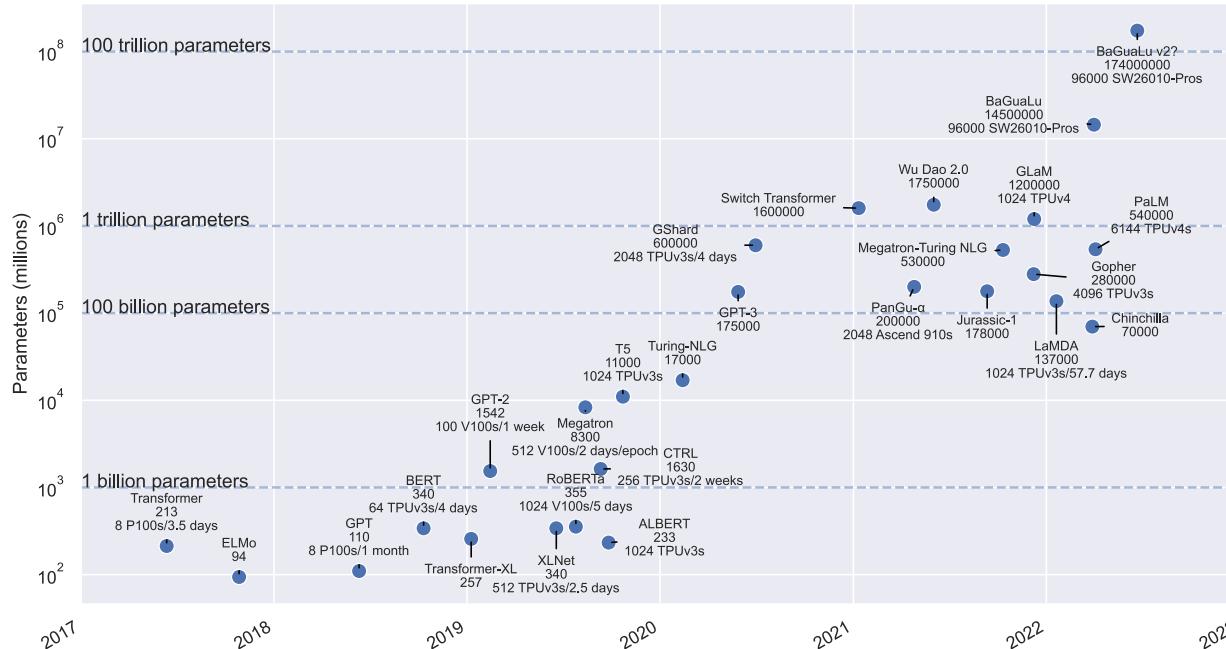
Gradients

Optimizer
state

400 TB

(n/a)

The Memory Explosion



Memory Usage

100 trillion parameters, FP32, Adam

Parameters

Activations

Gradients

Optimizer
state

400 TB

(n/a)

400 TB

The Memory Explosion



Memory Usage

100 trillion parameters, FP32, Adam

Parameters

Activations

Gradients

Optimizer
state

400 TB

(n/a)

400 TB

800 TB

The Memory Explosion



Memory Usage

100 trillion parameters, FP32, Adam

Parameters

Activations

Gradients

Optimizer
state

400 TB

(n/a)

400 TB

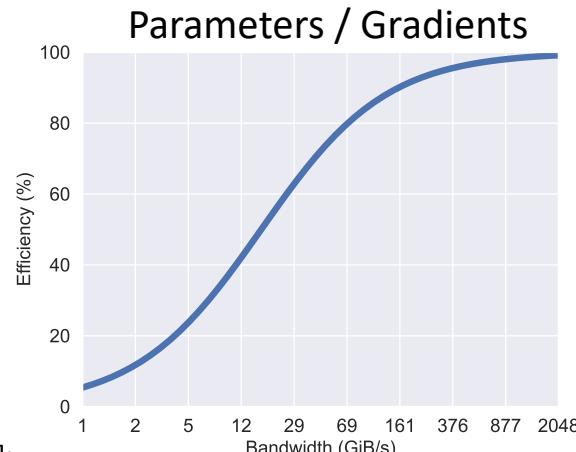
800 TB

= 1.6 PB

The Memory Explosion



Memory Bandwidth:



(Adapted from ZeRO-Infinity [Rajbhandari et al., 2021];
batch size 4, seqlen 1024, hidden dim 8K, 70 Tflop peak)

Memory Usage

100 trillion parameters, FP32, Adam

Parameters

Activations

Gradients

Optimizer state

400 TB

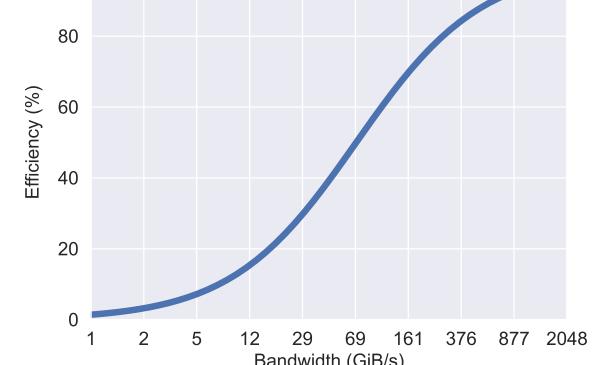
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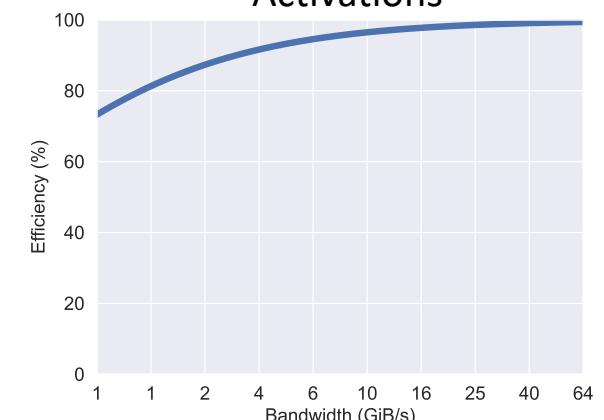
800 TB

= 1.6 PB

Optimizer State



Activations



How to Break the Memory Wall

How to Break the Memory Wall

How to Break the Memory Wall

Recomputation

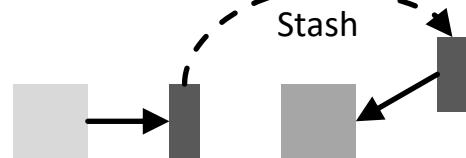
How to Break the Memory Wall

Recomputation



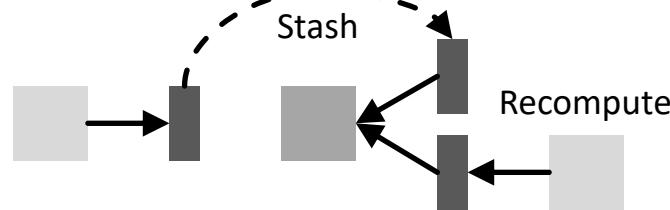
How to Break the Memory Wall

Recomputation



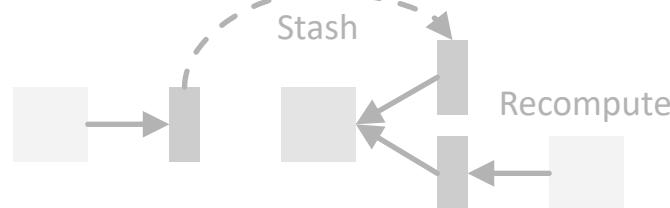
How to Break the Memory Wall

Recomputation



How to Break the Memory Wall

Recomputation

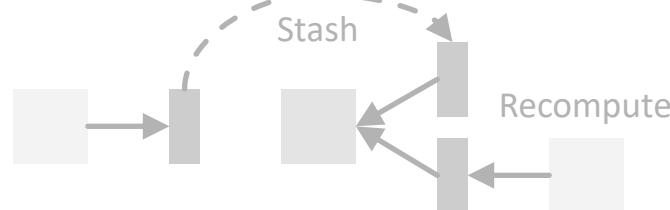


Out-of-core

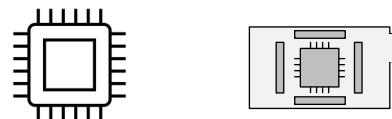
Systems

How to Break the Memory Wall

Recomputation

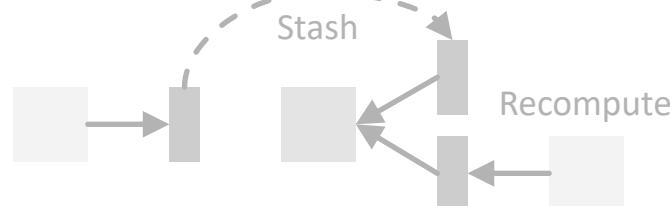


Out-of-core

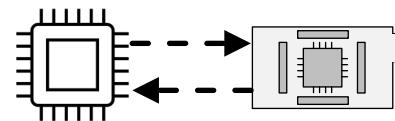


How to Break the Memory Wall

Recomputation

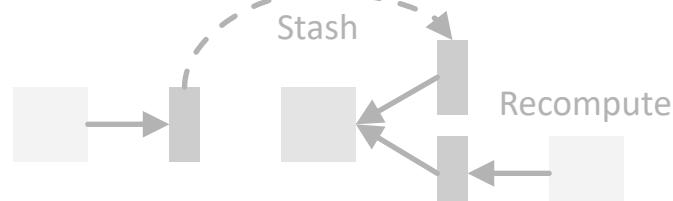


Out-of-core

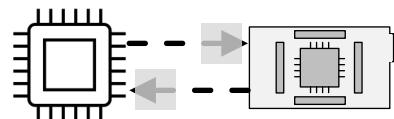


How to Break the Memory Wall

Recomputation

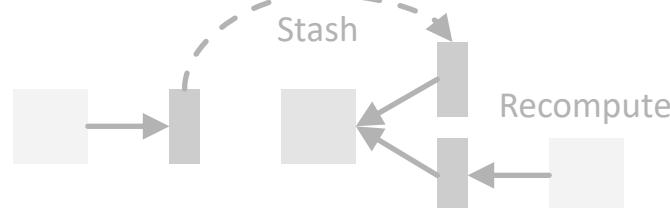


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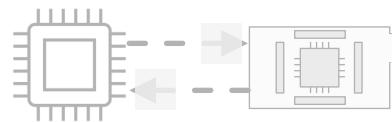


How to Break the Memory Wall

Recomputation



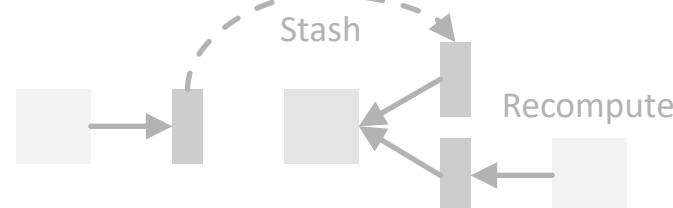
Out-of-core



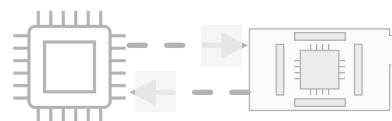
Model parallelism

How to Break the Memory Wall

Recomputation



Out-of-core

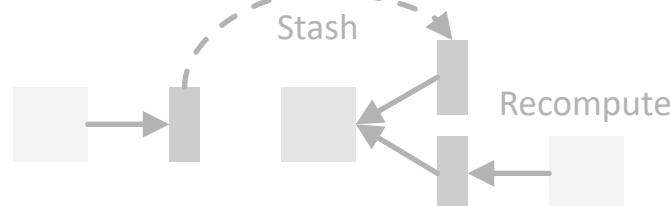


Model parallelism

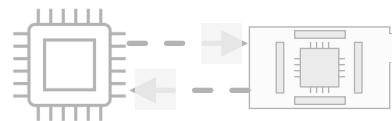


How to Break the Memory Wall

Recomputation



Out-of-core

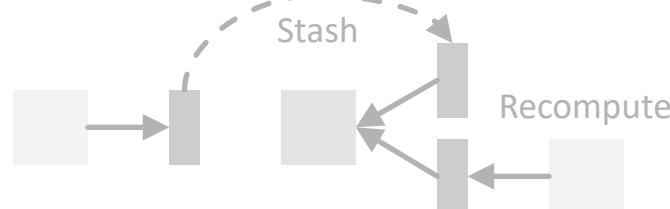


Model parallelism

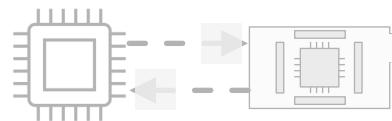


How to Break the Memory Wall

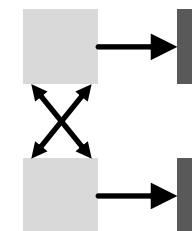
Recomputation



Out-of-core

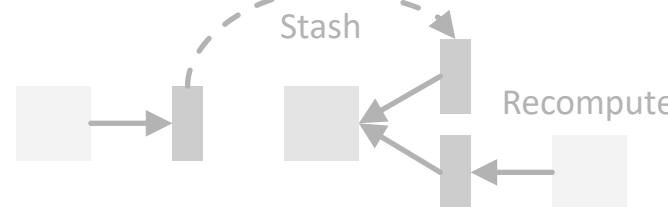


Model parallelism

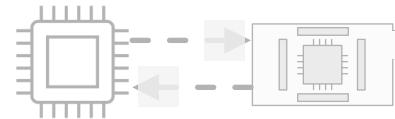


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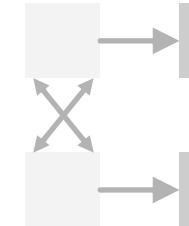
Recomputation



Out-of-core



Model parallelism

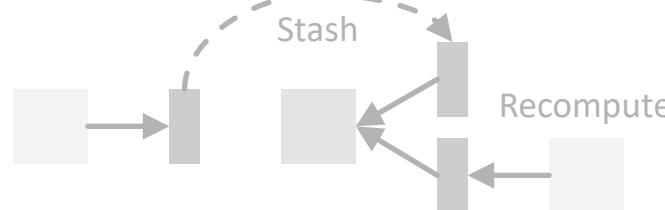


Systems

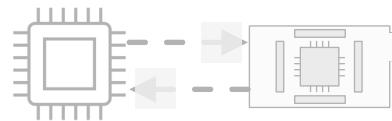
Models

How to Break the Memory Wall

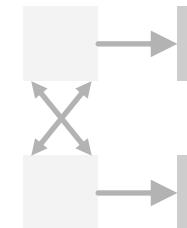
Recomputation



Out-of-core



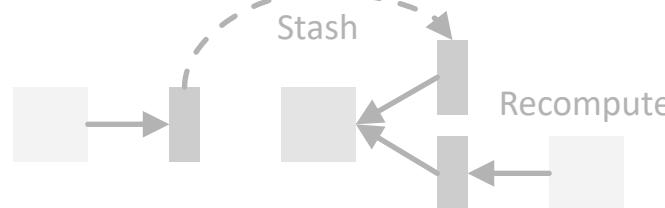
Model parallelism



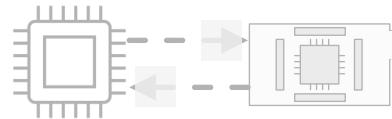
Quantization / Pruning /
Low-rank / Distillation

How to Break the Memory Wall

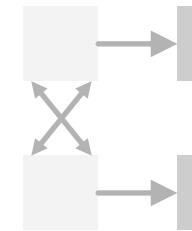
Recomputation



Out-of-core



Model parallelism

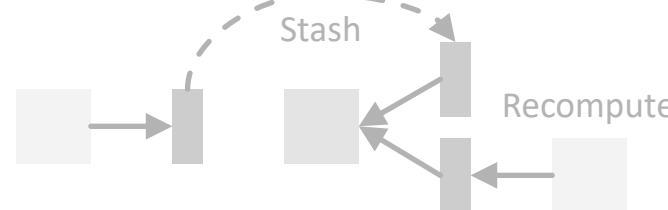


Quantization / Pruning /
Low-rank / Distillation

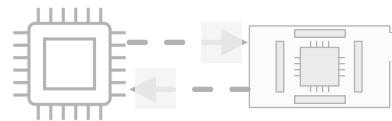


How to Break the Memory Wall

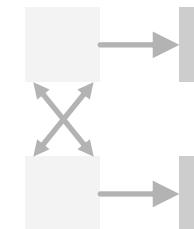
Recomputation



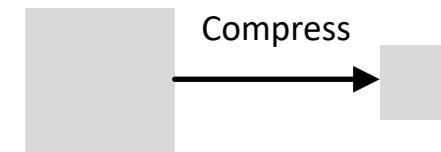
Out-of-core



Model parallelism

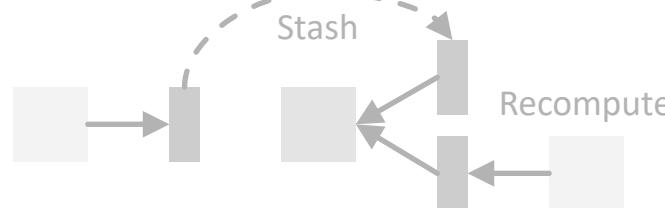


Quantization / Pruning /
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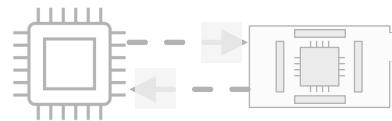


How to Break the Memory Wall

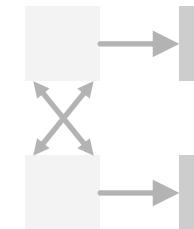
Recomputation



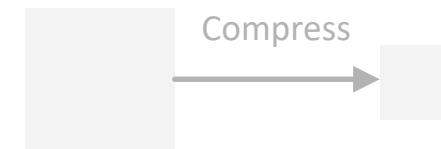
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



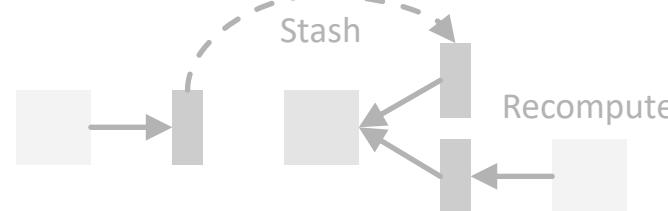
Parameter sharing

Systems

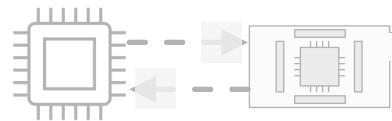
Models

How to Break the Memory Wall

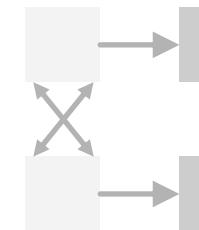
Recomputation



Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation

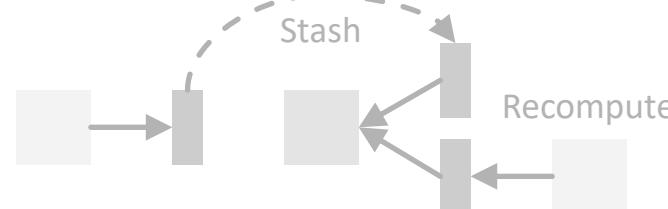


Parameter sharing

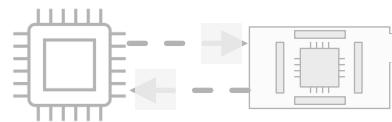


How to Break the Memory Wall

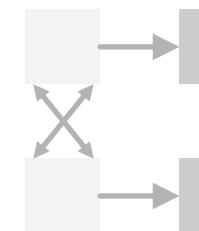
Recomputation



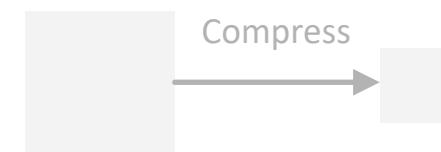
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation

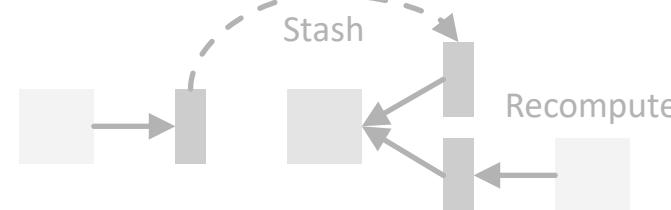


Parameter sharing

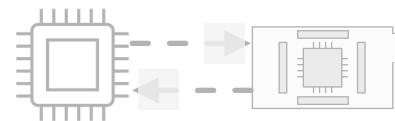


How to Break the Memory Wall

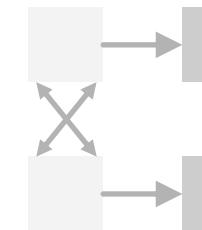
Recomputation



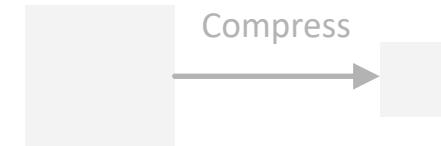
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



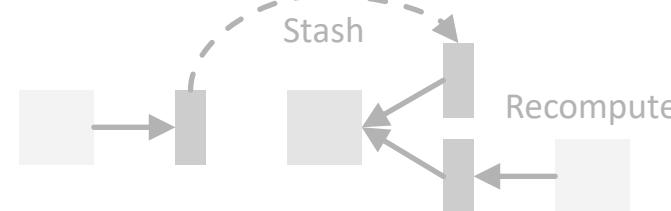
Parameter sharing



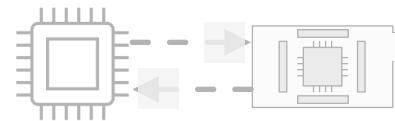
All of the above

How to Break the Memory Wall

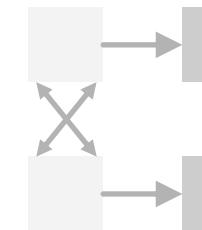
Recomputation



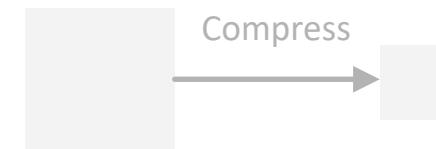
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



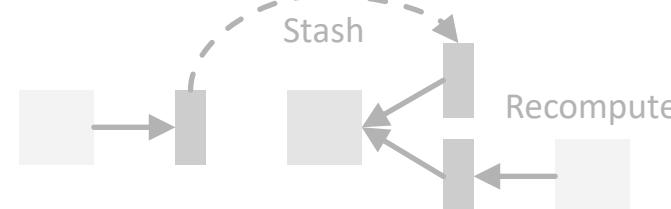
Parameter sharing



All of the above

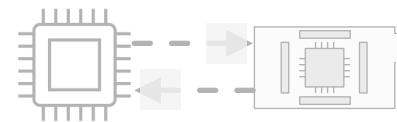
How to Break the Memory Wall

Recomputation

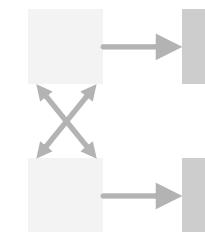


Limitations of Standard Parameter Sharing

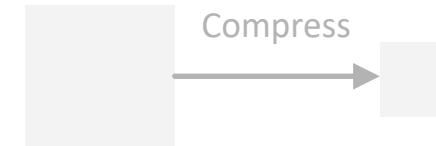
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



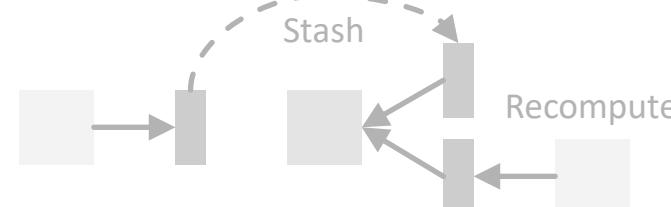
Parameter sharing



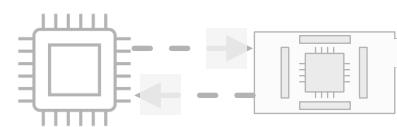
All of the above

How to Break the Memory Wall

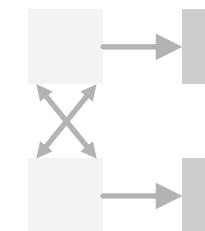
Recomputation



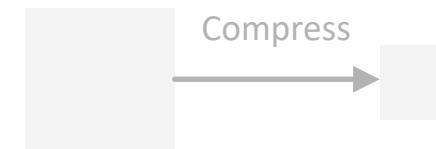
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



Parameter sharing



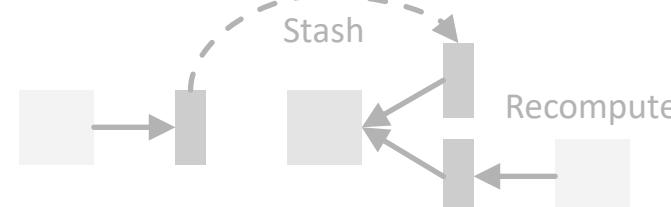
All of the above

Limitations of Standard Parameter Sharing

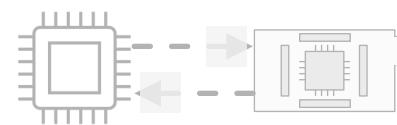
Only share between identical layers

How to Break the Memory Wall

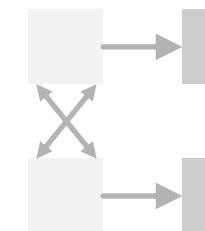
Recomputation



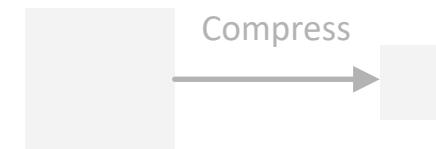
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



Parameter sharing



All of the above

Limitations of Standard Parameter Sharing

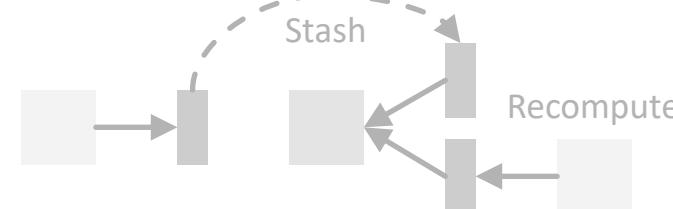
Only share between identical layers

1K

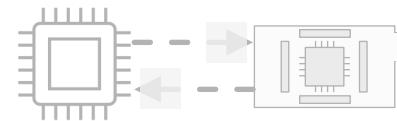
100K

How to Break the Memory Wall

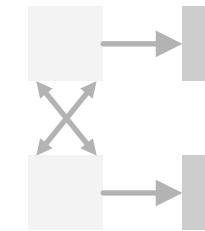
Recomputation



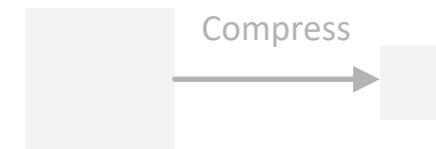
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation

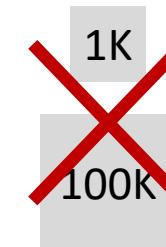


Parameter sharing



Limitations of Standard Parameter Sharing

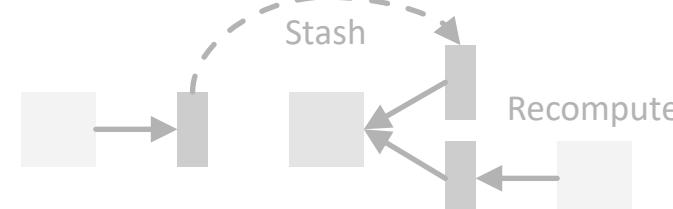
Only share between identical layers



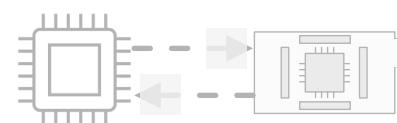
All of the above

How to Break the Memory Wall

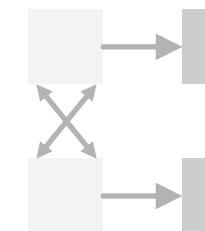
Recomputation



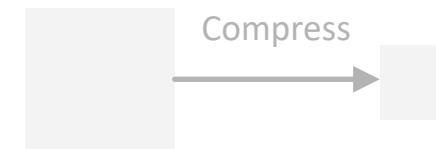
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



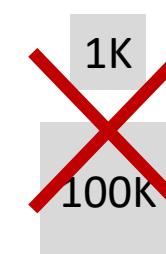
Parameter sharing



All of the above

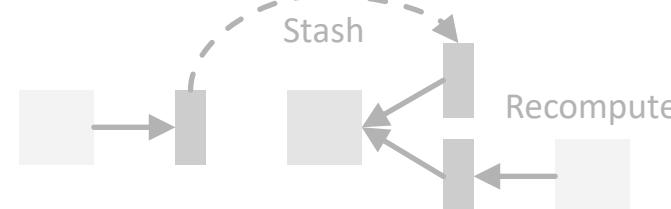
Limitations of Standard Parameter Sharing

Only share between identical layers

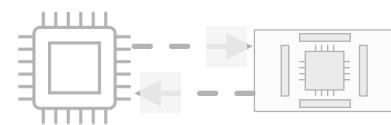


How to Break the Memory Wall

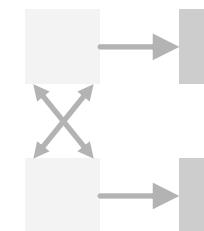
Recomputation



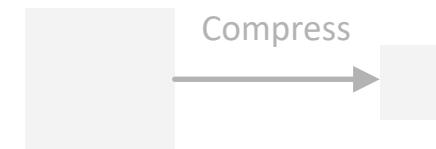
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



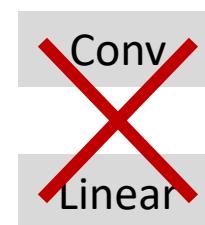
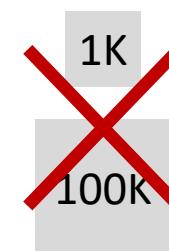
Parameter sharing



All of the above

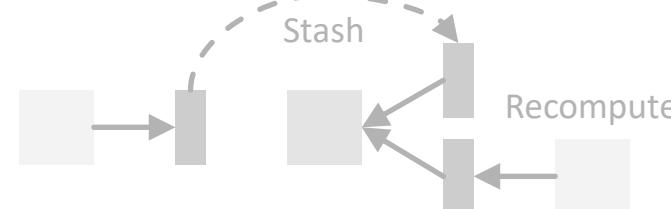
Limitations of Standard Parameter Sharing

Only share between identical layers

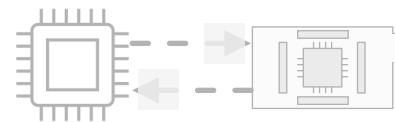


How to Break the Memory Wall

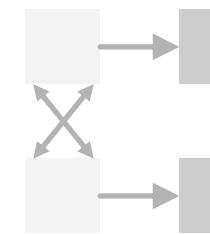
Recomputation



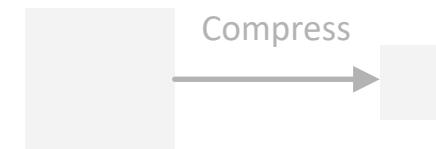
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



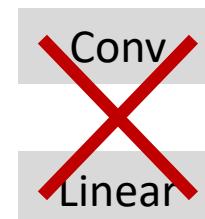
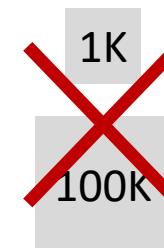
Parameter sharing



All of the above

Limitations of Standard Parameter Sharing

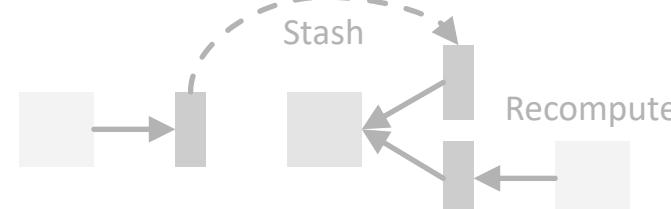
Only share between identical layers



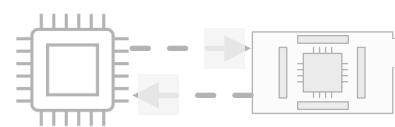
Does not support arbitrary parameter budgets

How to Break the Memory Wall

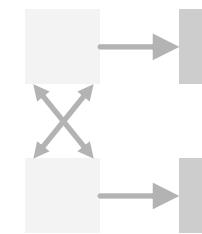
Recomputation



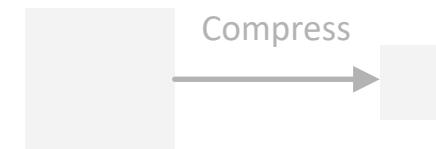
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



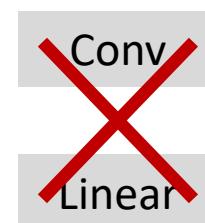
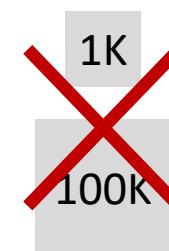
Parameter sharing



All of the above

Limitations of Standard Parameter Sharing

Only share between identical layers



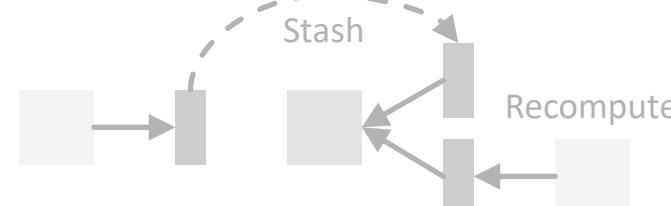
Does not support arbitrary parameter budgets

Budget

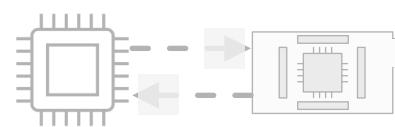
10K

How to Break the Memory Wall

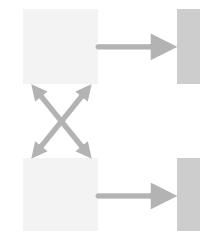
Recomputation



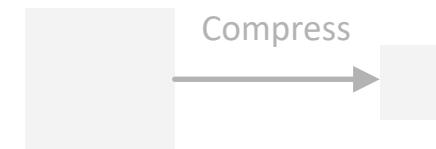
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



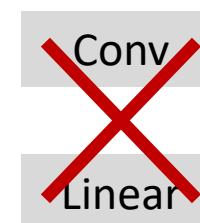
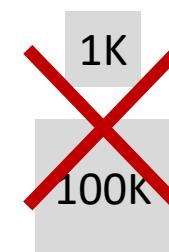
Parameter sharing



All of the above

Limitations of Standard Parameter Sharing

Only share between identical layers

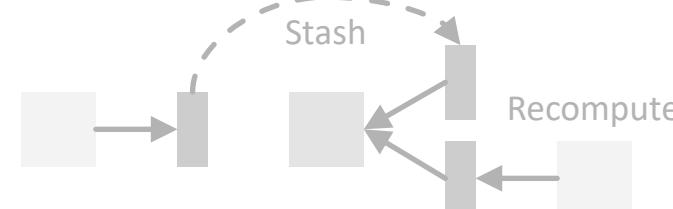


Does not support arbitrary parameter budgets

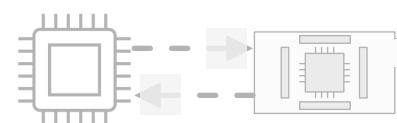
Budget	Layer
10K	100K

How to Break the Memory Wall

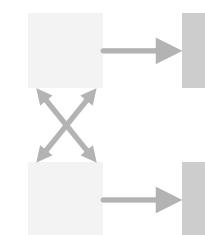
Recomputation



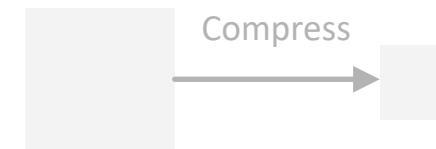
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



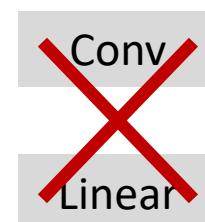
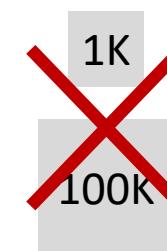
Parameter sharing



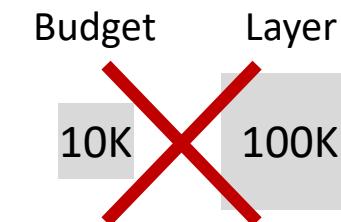
All of the above

Limitations of Standard Parameter Sharing

Only share between identical layers

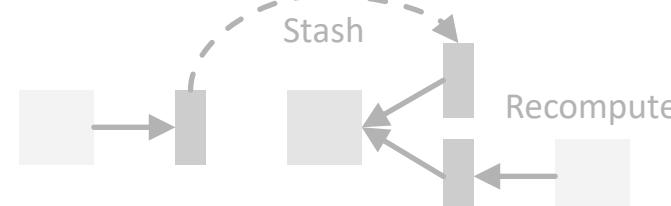


Does not support arbitrary parameter budgets

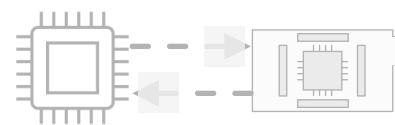


How to Break the Memory Wall

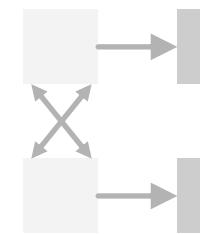
Recomputation



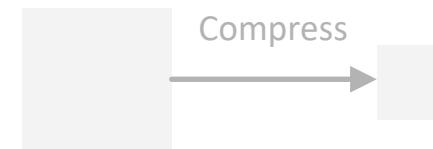
Out-of-core



Model parallelism



Quantization / Pruning /
Low-rank / Distillation



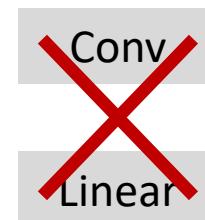
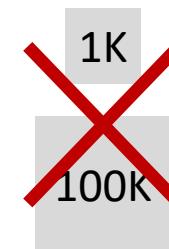
Parameter sharing



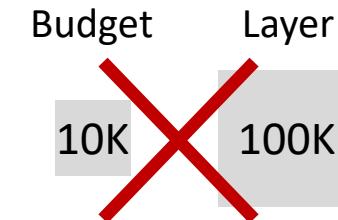
All of the above

Limitations of Standard Parameter Sharing

Only share between identical layers



Does not support arbitrary parameter budgets



Sharing is manually designed

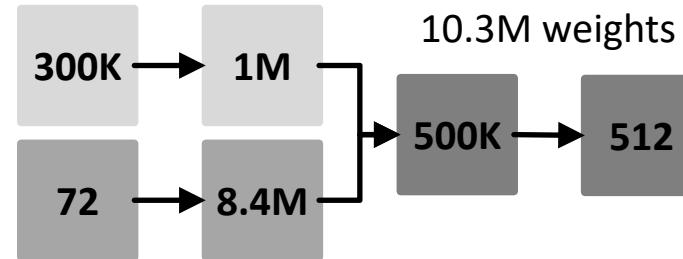
Neural Parameter Allocation Search (NPAS)

Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.

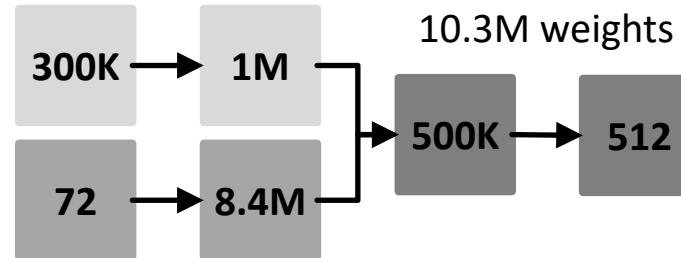
Neural Parameter Allocation Search (NPAS)

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Neural Parameter Allocation Search (NPAS)

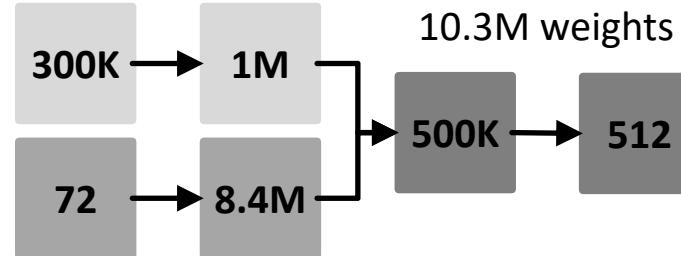
Using *any* parameter budget, train a high-performing neural network using that parameter budget.



Low-budget NPAS

Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



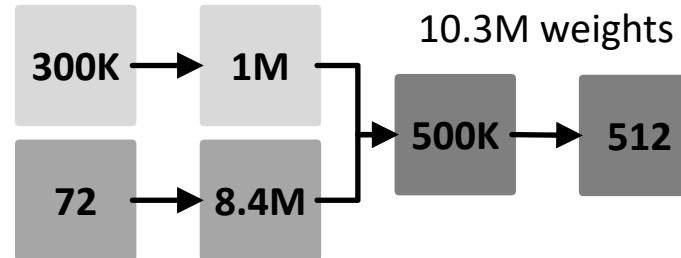
Low-budget NPAS

Parameters

2M

Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



Low-budget NPAS

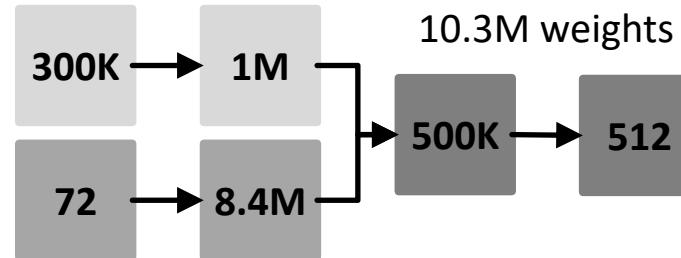
High-budget NPAS

Parameters

2M

Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



Low-budget NPAS

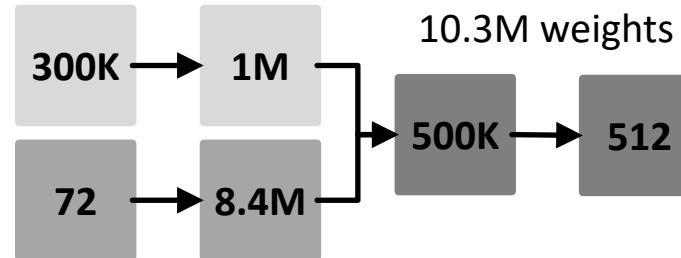
Parameters
2M

High-budget NPAS

Parameters
20M

Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



Low-budget NPAS

Parameters



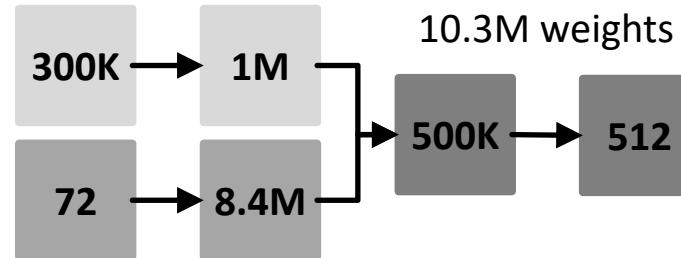
High-budget NPAS

Parameters



Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



1. Parameter mapping

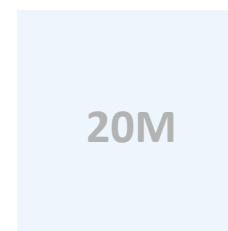
Low-budget NPAS

High-budget NPAS

Parameters

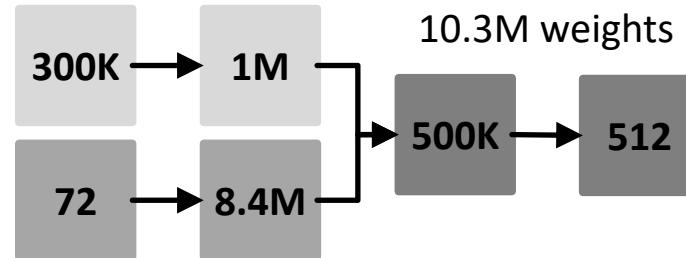


Parameters

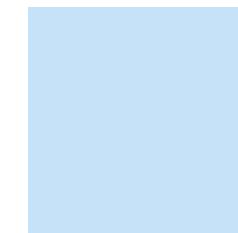


Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



1. Parameter mapping



Low-budget NPAS

High-budget NPAS

Parameters

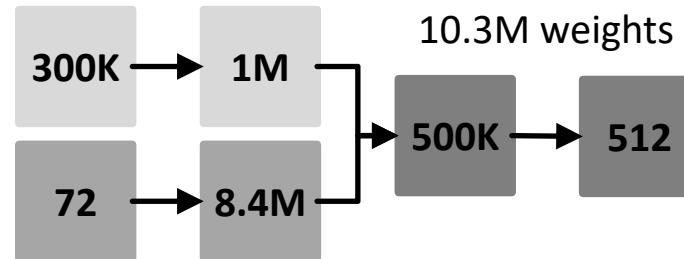


Parameters



Neural Parameter Allocation Search (NPAS)

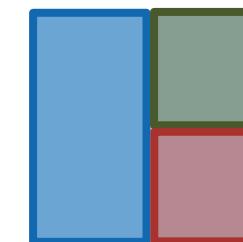
Using *any* parameter budget, train a high-performing neural network using that parameter budget.



Low-budget NPAS

High-budget NPAS

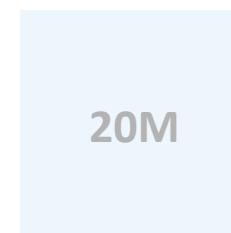
1. Parameter mapping



Parameters

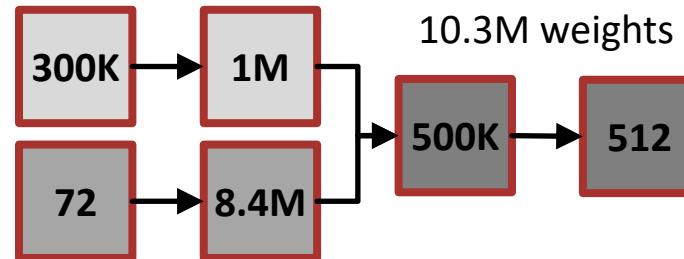


Parameters



Neural Parameter Allocation Search (NPAS)

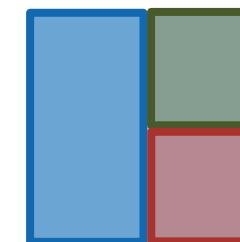
Using *any* parameter budget, train a high-performing neural network using that parameter budget.



Low-budget NPAS

High-budget NPAS

1. Parameter mapping



Parameters

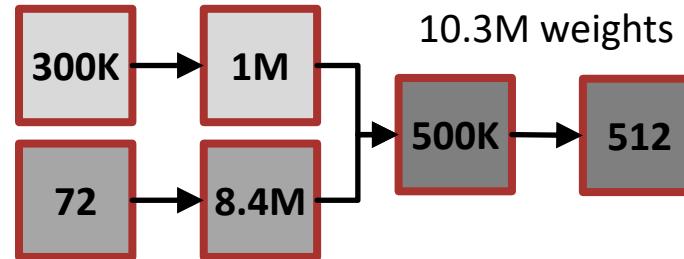


Parameters



Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



Low-budget NPAS

Parameters

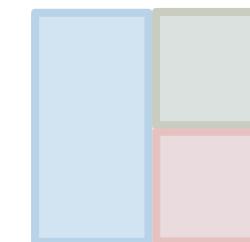


High-budget NPAS

Parameters

20M

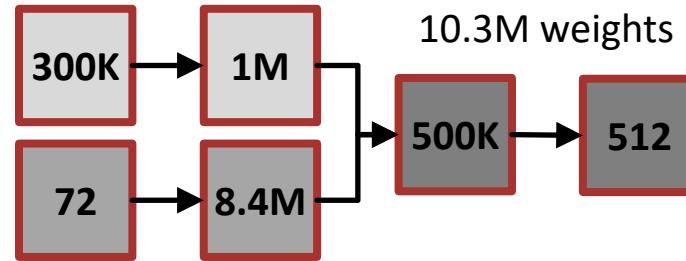
1. Parameter mapping



2. Weight generation

Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



Low-budget NPAS

High-budget NPAS

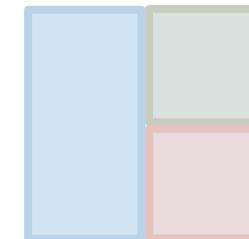
Parameters



Parameters



1. Parameter mapping

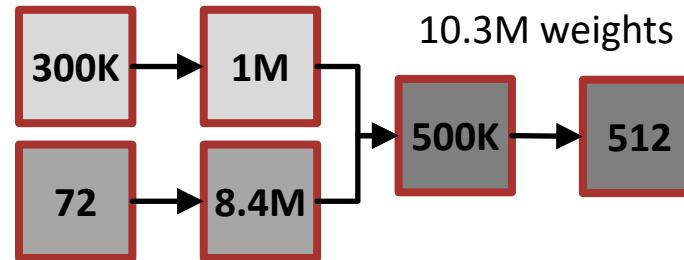


2. Weight generation



Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



Low-budget NPAS

High-budget NPAS

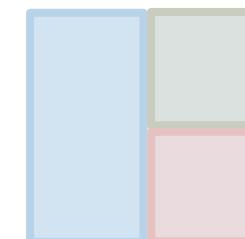
Parameters



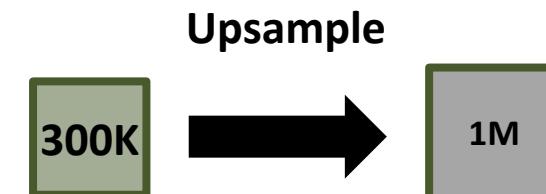
Parameters



1. Parameter mapping

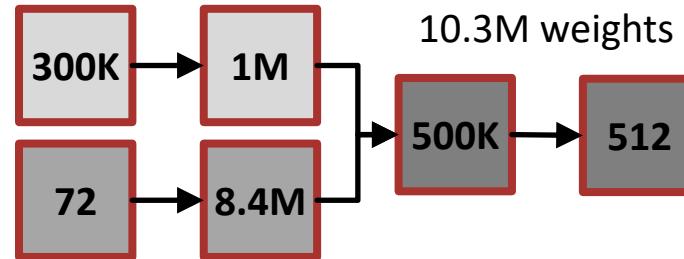


2. Weight generation



Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



Low-budget NPAS

High-budget NPAS

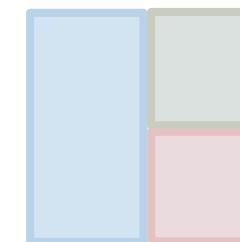
Parameters



Parameters



1. Parameter mapping

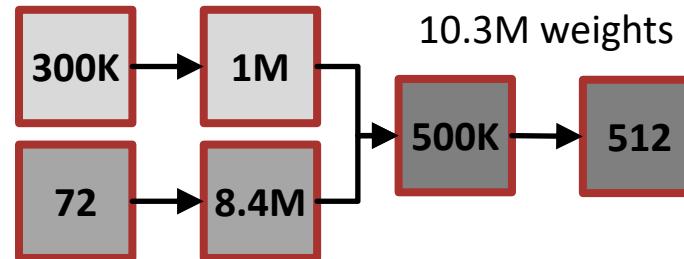


2. Weight generation



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Low-budget NPAS

High-budget NPAS

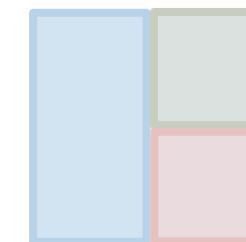
Parameters



Parameters



1. Parameter mapping

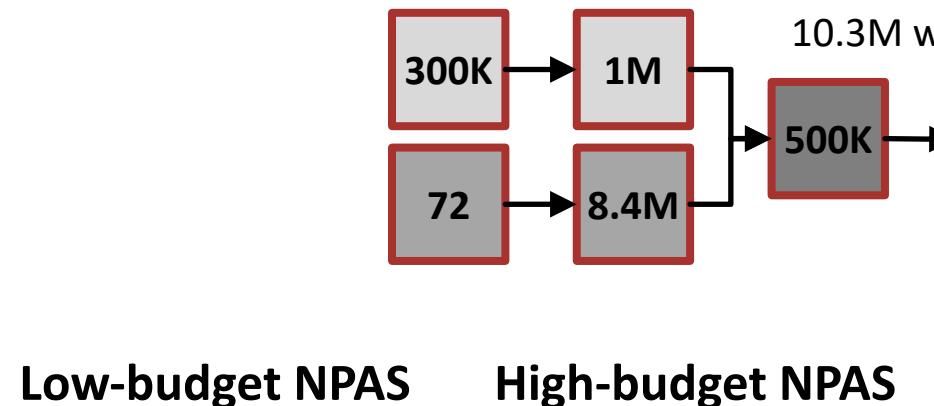


2. Weight generation

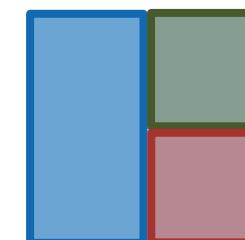


Neural Parameter Allocation Search (NPAS)

Using *any* parameter budget, train a high-performing neural network using that parameter budget.



1. Parameter mapping



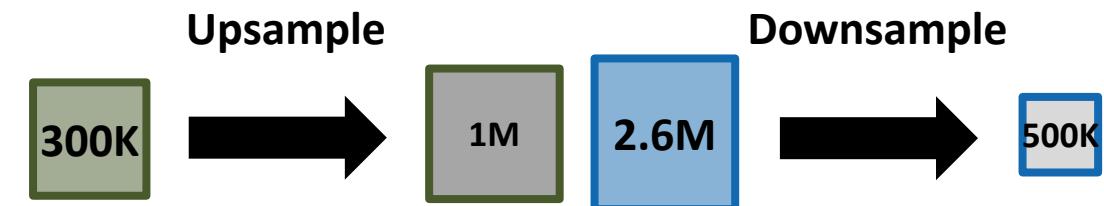
Parameters

2M

Parameters

20M

2. Weight generation



Advantages of Parameter Sharing

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Parameter sharing reduces memory during training and inference
(In the low-budget case)

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The same model ...

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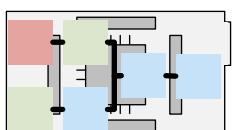
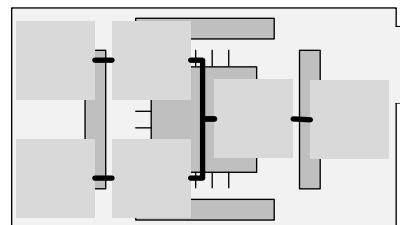
Fits in smaller devices

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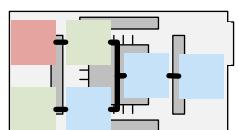
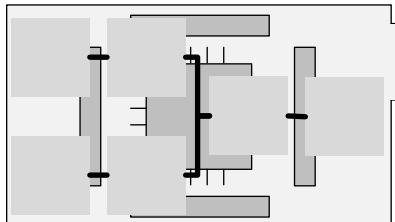


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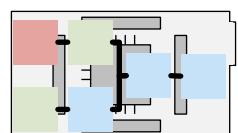
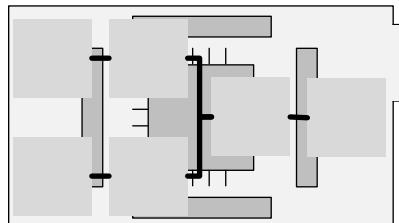
Uses less communication

Advantages of Parameter Sharing

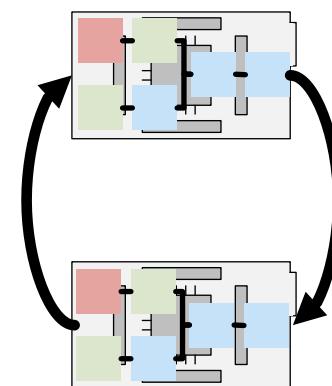
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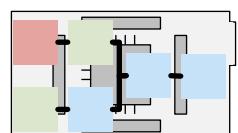
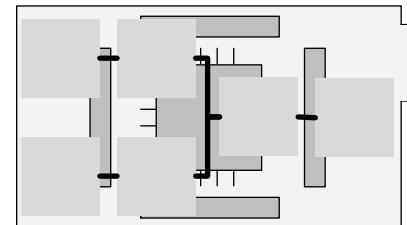


Advantages of Parameter Sharing

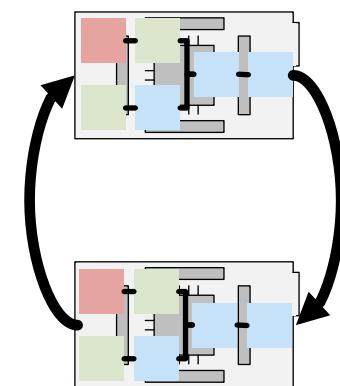
Parameter sharing reduces memory during training and inference
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The same model ...

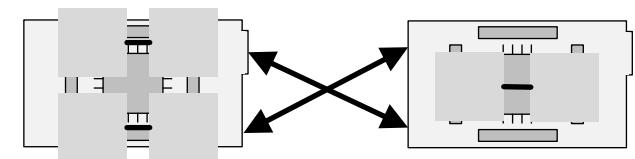
Fits in smaller devices



Uses less communication



Needs less model-parallelism

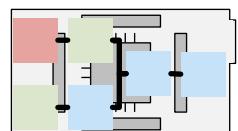
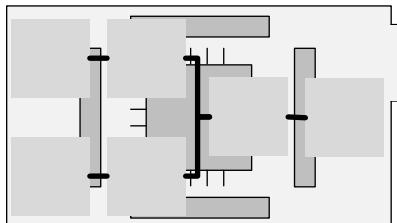


Advantages of Parameter Sharing

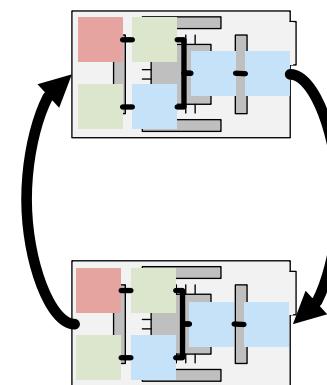
Parameter sharing reduces memory during training and inference
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The same model ...

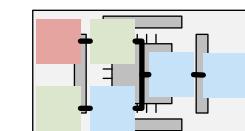
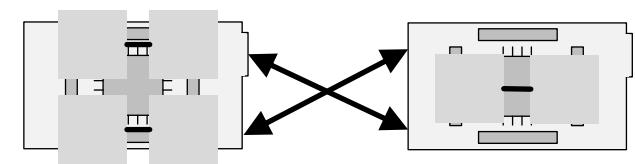
Fits in smaller devices



Uses less communication

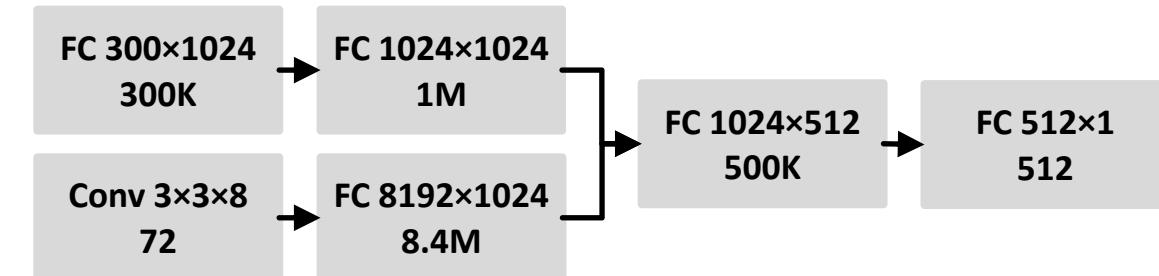


Needs less model-parallelism

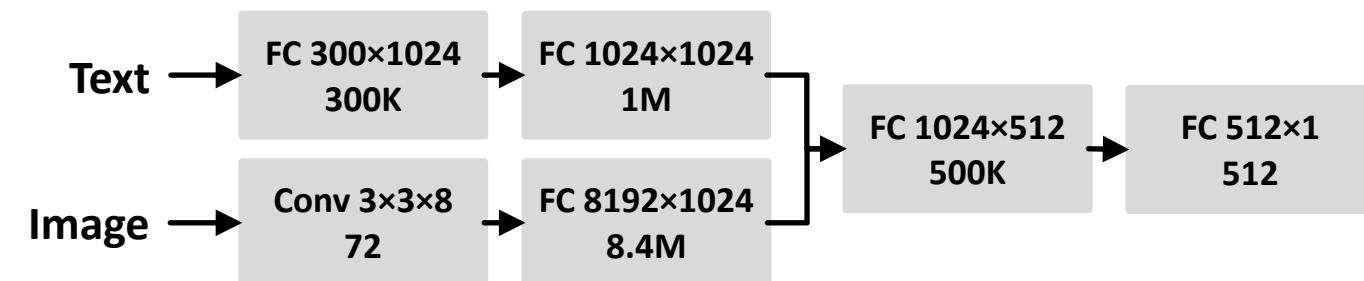


Shapeshifter Networks (SSNs)

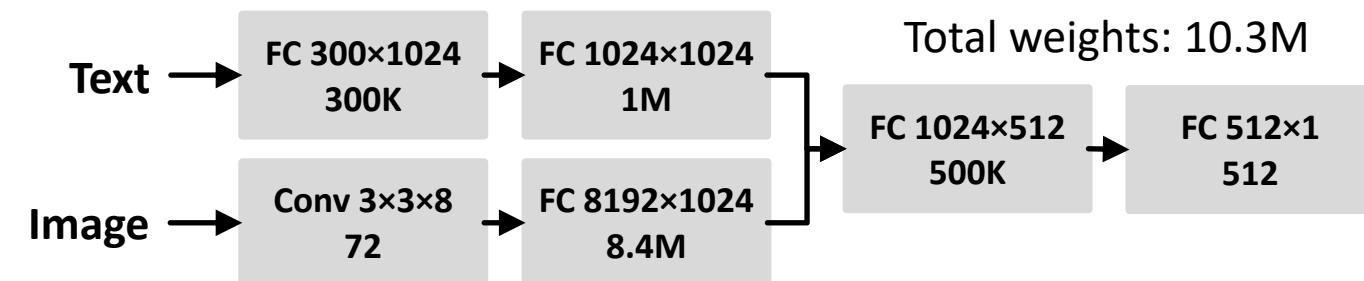
Shapeshifter Networks (SSNs)



Shapeshifter Networks (SSNs)

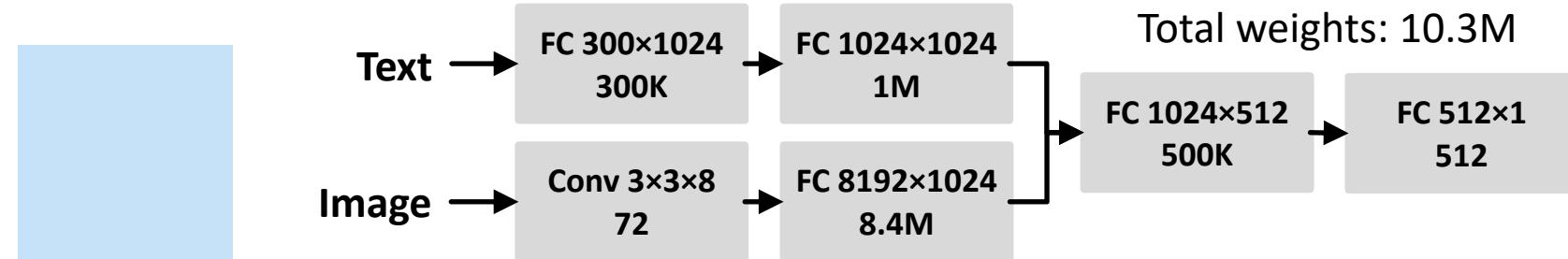


Shapeshifter Networks (SSNs)



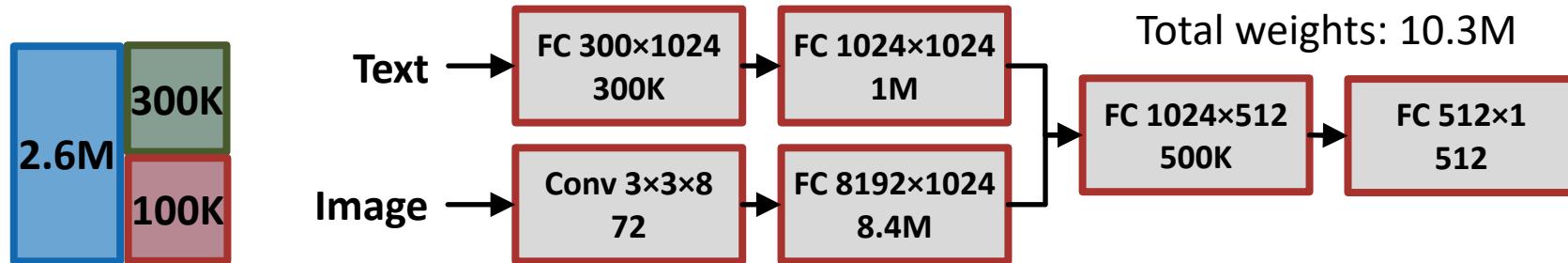
Shapeshifter Networks (SSNs)

Parameter budget: 3M



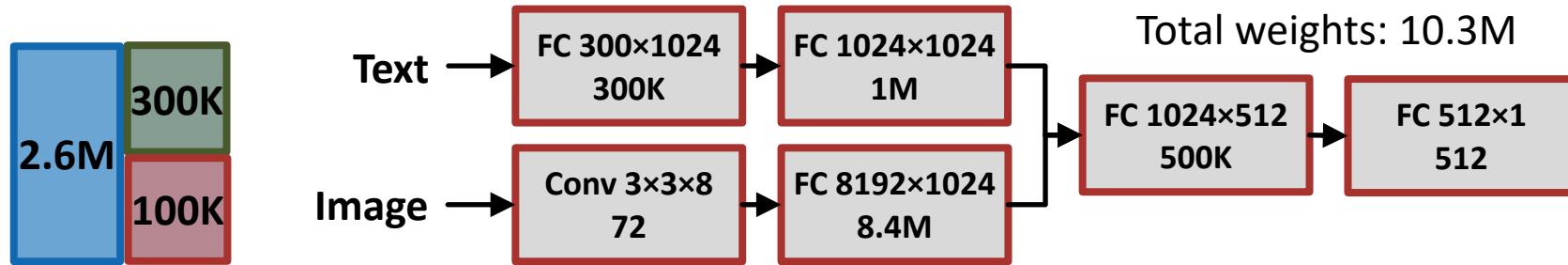
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



Shapeshifter Networks (SSNs)

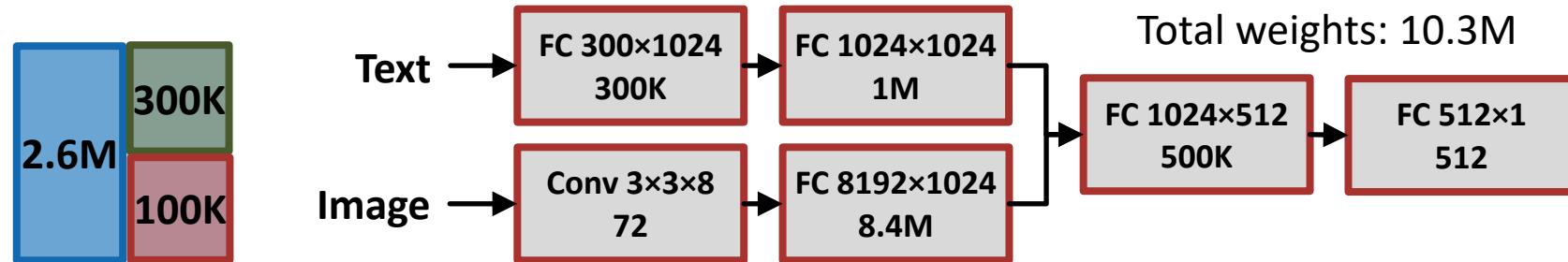
Parameter budget: 3M
 $P = 3$ parameter groups



Weight Generation

Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups

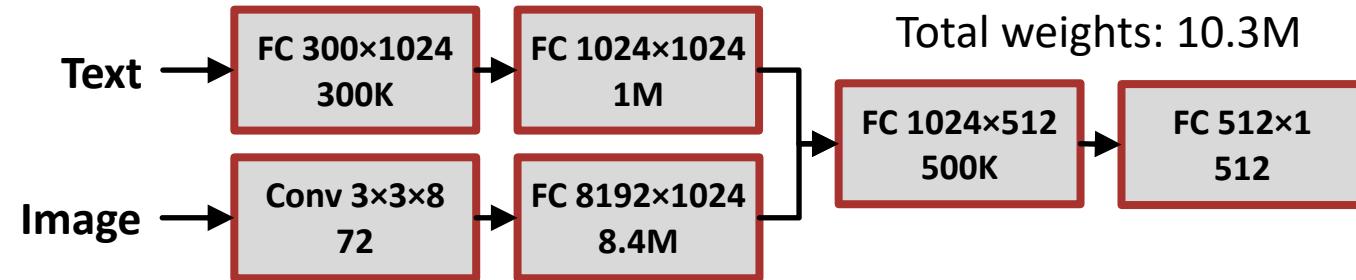
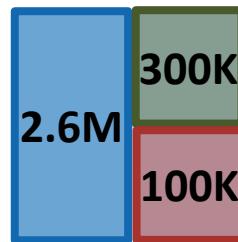


Weight Generation

Upsample:

Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups

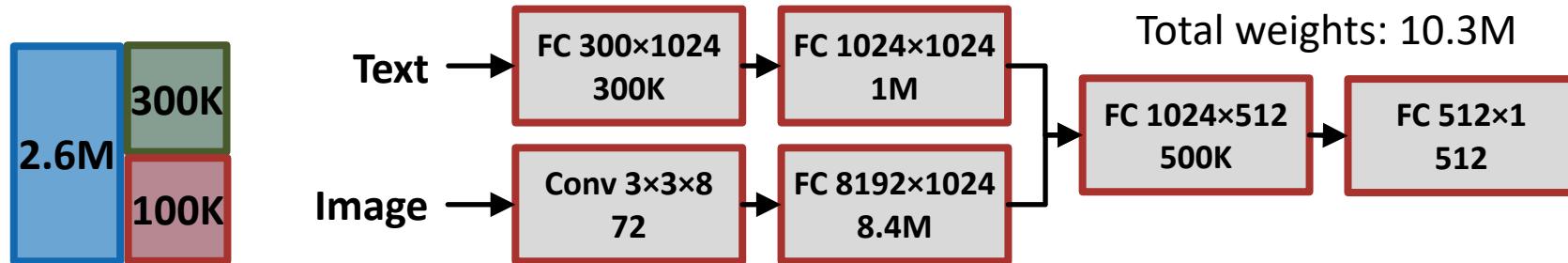


Weight Generation

Upsample: Interpolate

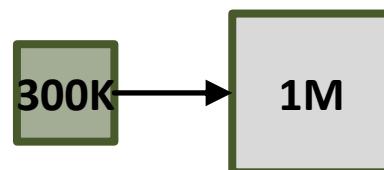
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



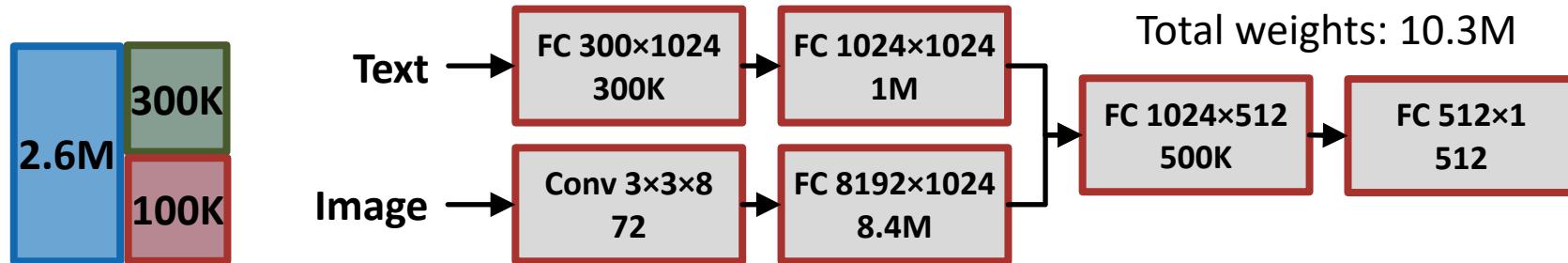
Weight Generation

Upsample: Interpolate



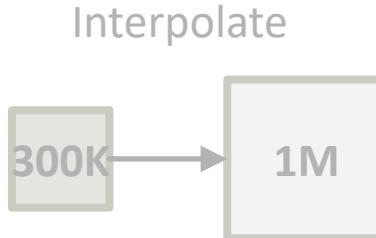
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



Weight Generation

Upsample:

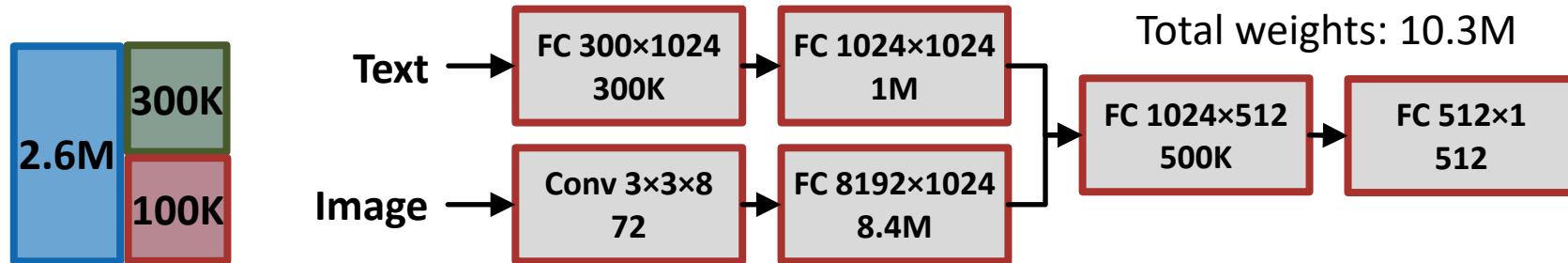


Interpolate

Mask

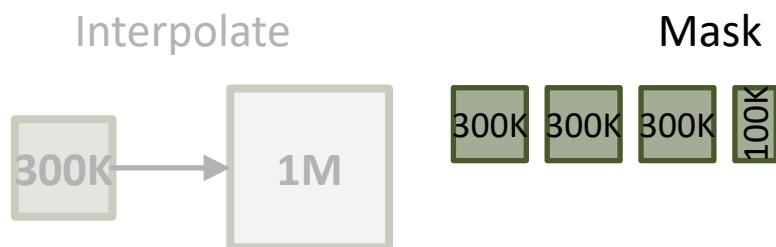
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



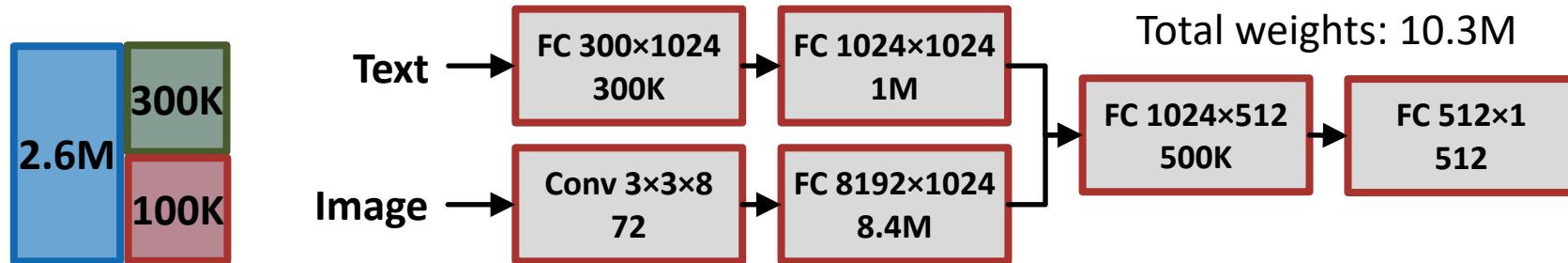
Weight Generation

Upsample:



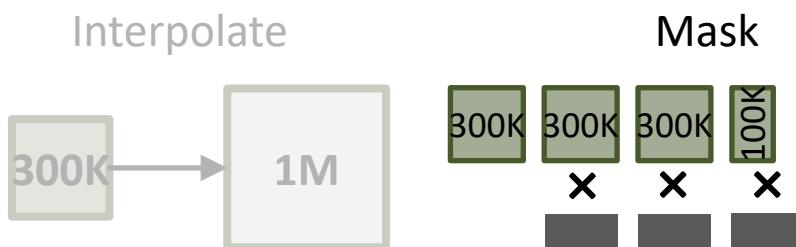
Shapeshifter Networks (SSNs)

Parameter budget: 3M
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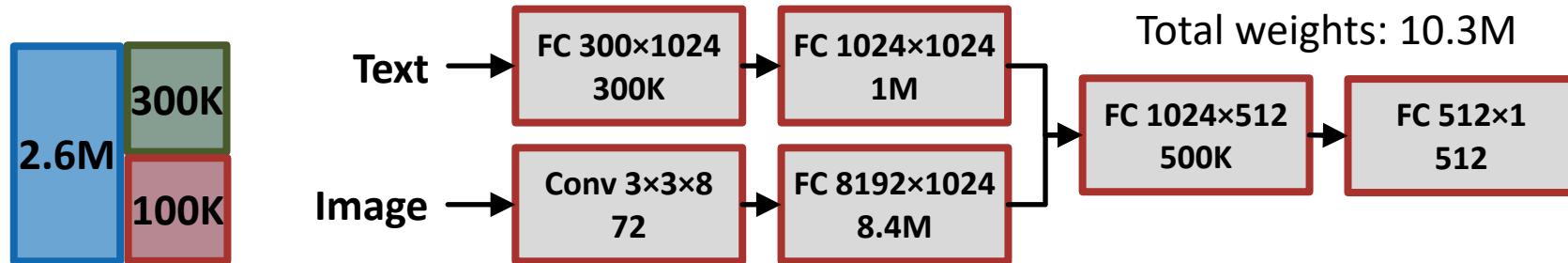
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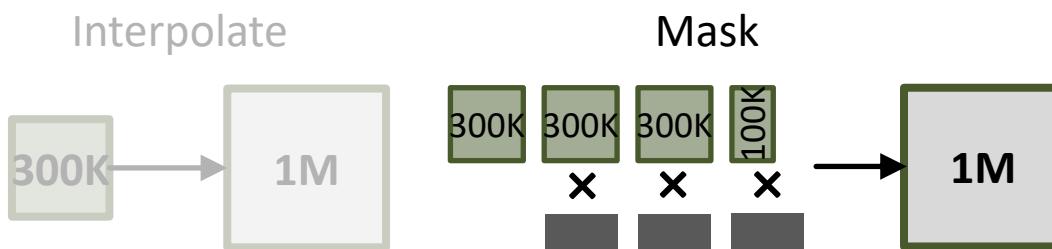
Shapeshifter Networks (SSNs)

Parameter budget: 3M
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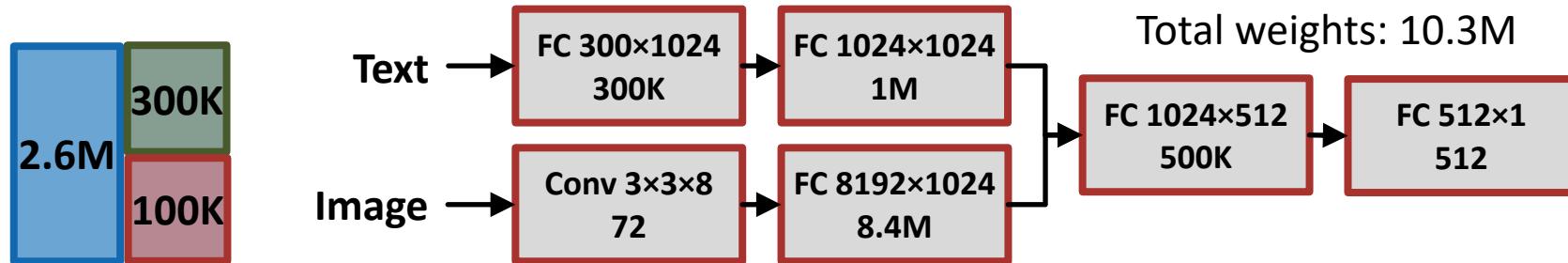
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Upsample:



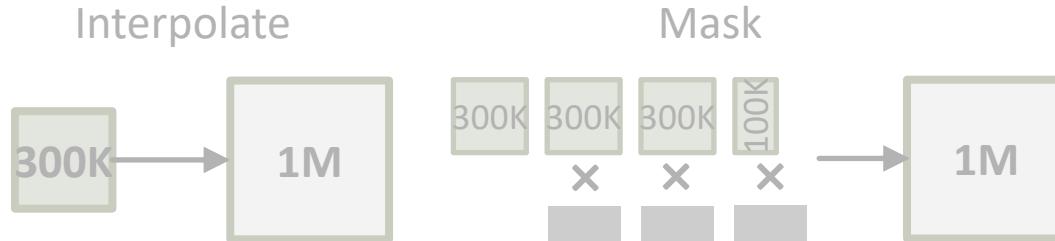
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



Weight Generation

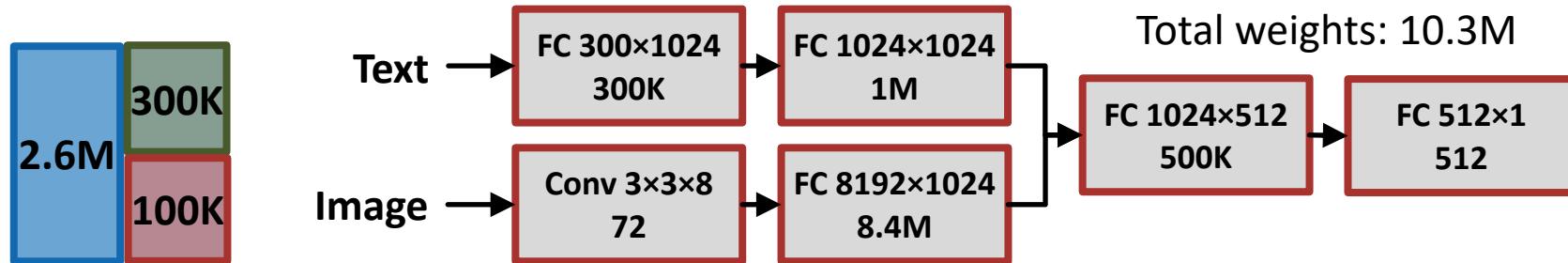
Upsample:



Downsample:

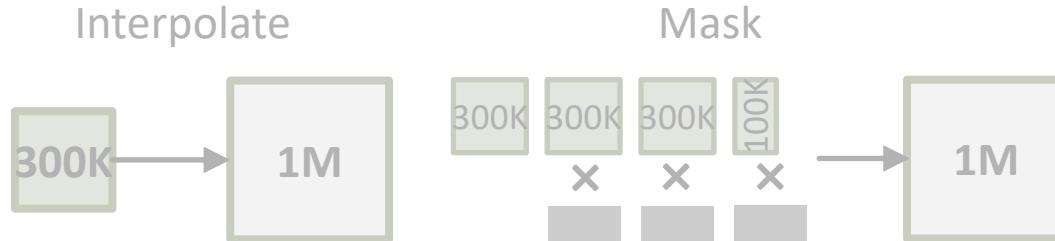
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



Weight Generation

Upsample:

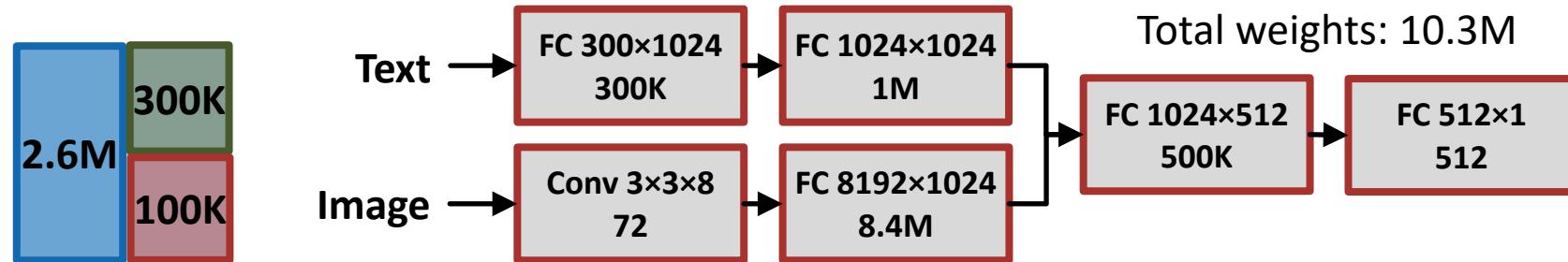


Downsample:



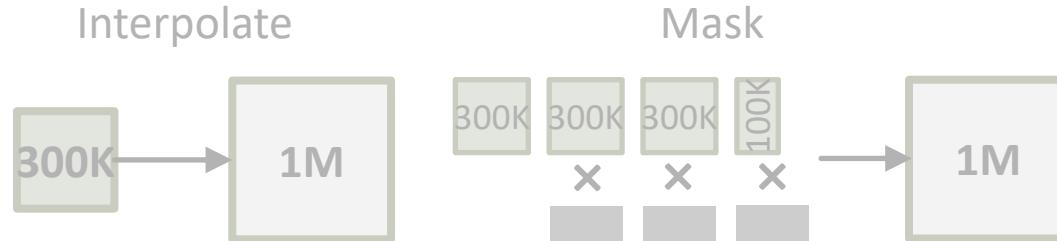
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



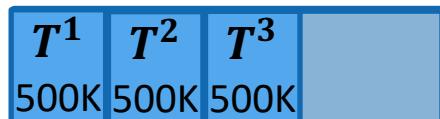
Weight Generation

Upsample:



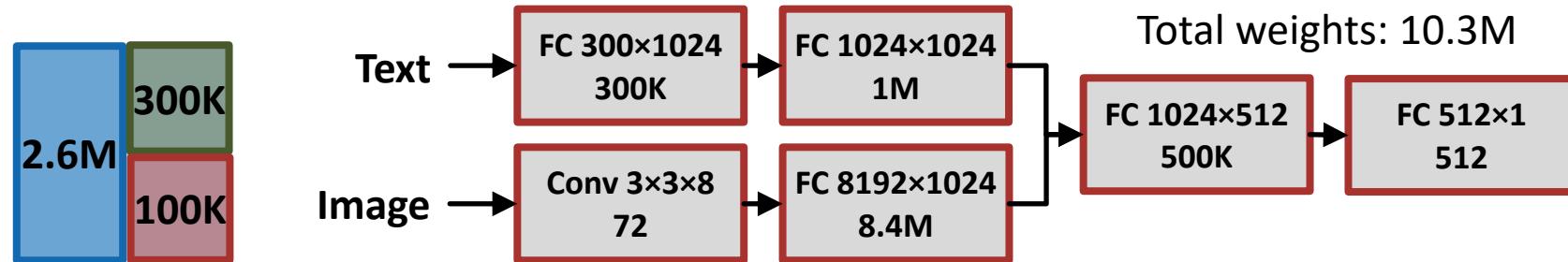
Downsample:

$K = 3$ templates



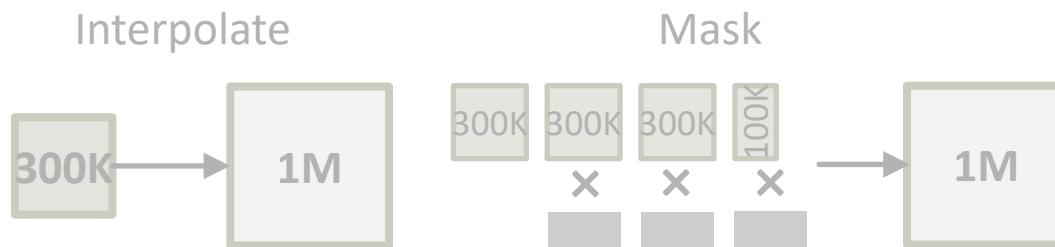
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



Weight Generation

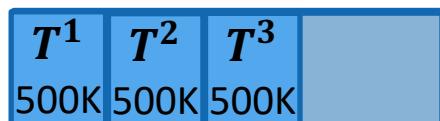
Upsample:



Downsample:

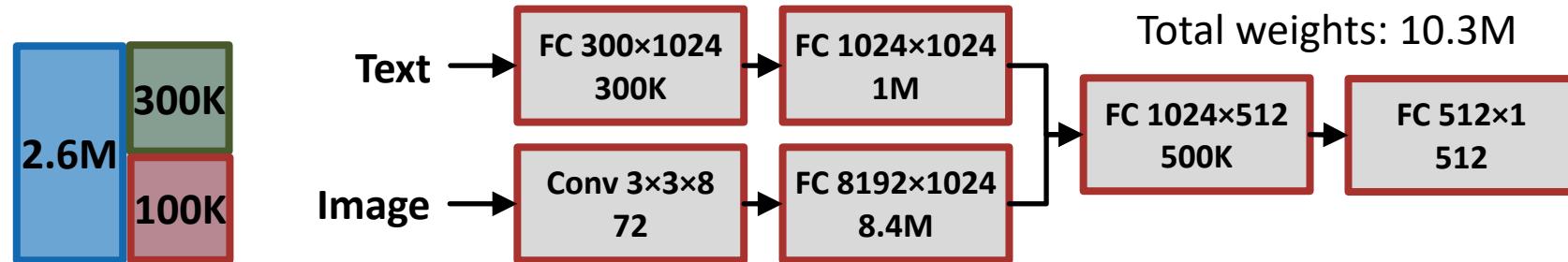
WAvg
[Savarese & Maire, 2019]

$K = 3$ templates



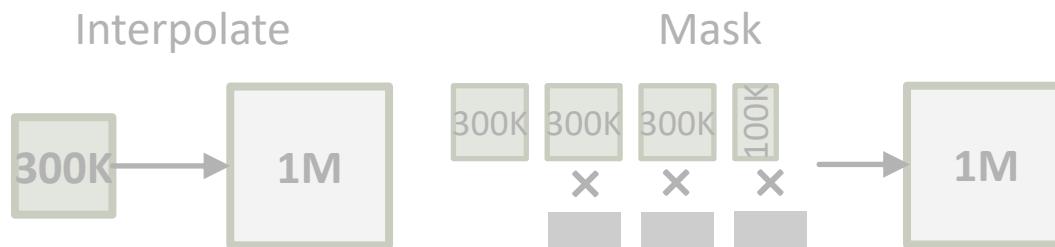
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



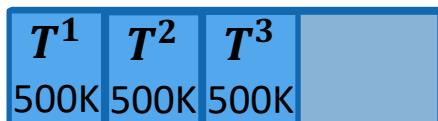
Weight Generation

Upsample:



Downsample:

$K = 3$ templates

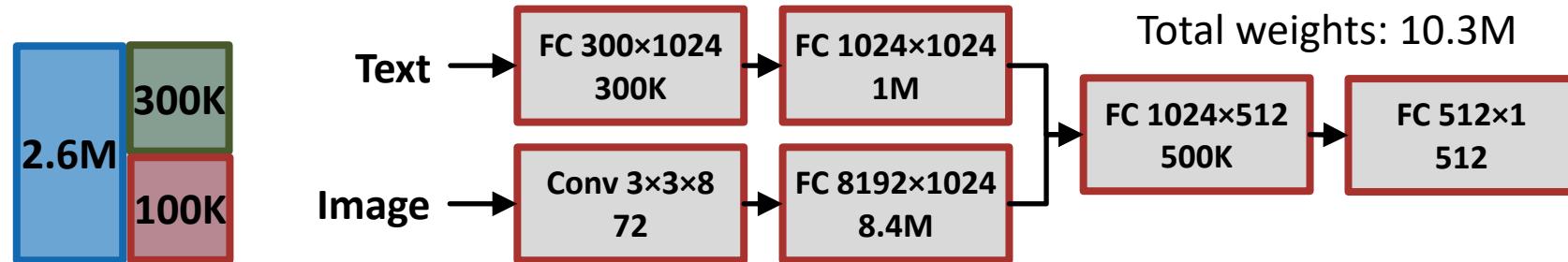


WAvg
[Savarese & Maire, 2019]

Coefficients α_i

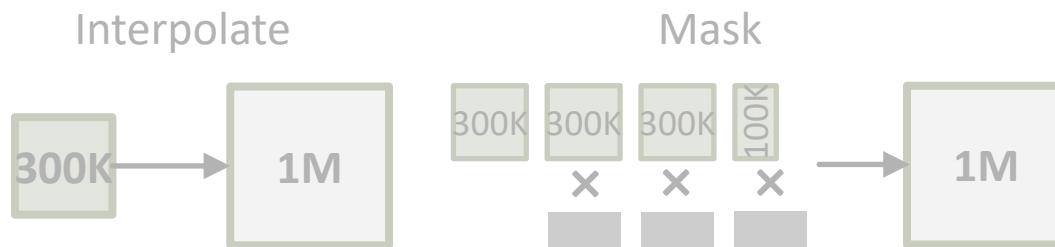
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



Weight Generation

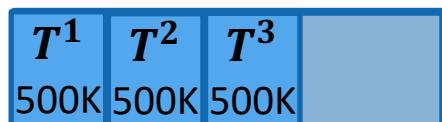
Upsample:



Downsample:

WAvg
[Savarese & Maire, 2019]

$K = 3$ templates

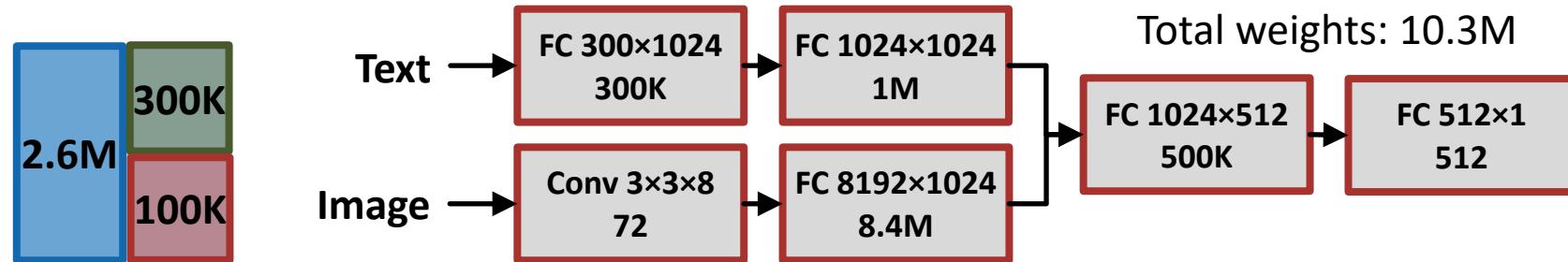


Coefficients α_i

$$500K = \alpha_1^1 T^1_{500K} + \alpha_2^2 T^2_{500K} + \alpha_3^3 T^3_{500K}$$

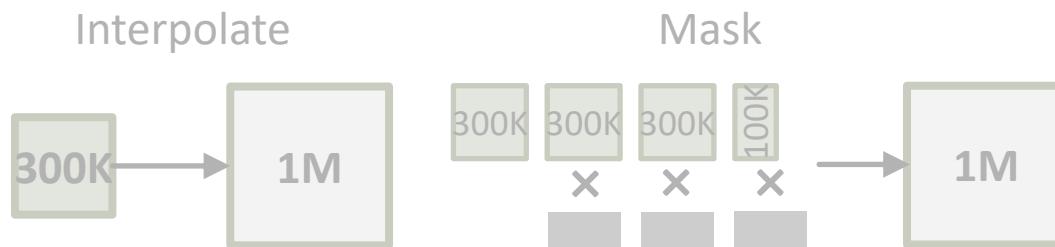
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



Weight Generation

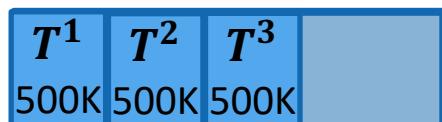
Upsample:



Downsample:

WAvg
 [Savarese & Maire, 2019]

$K = 3$ templates

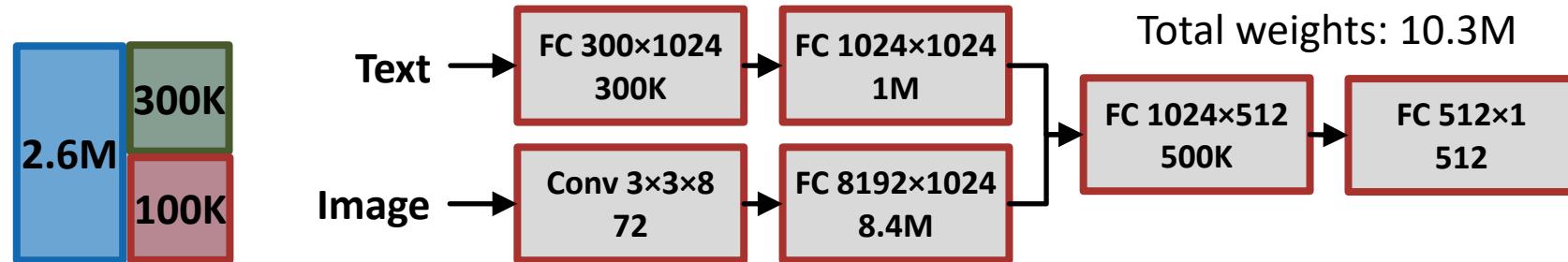


Coefficients α_i

$$500K = \alpha_i^1 T^1_{500K} + \alpha_i^2 T^2_{500K} + \alpha_i^3 T^3_{500K}$$

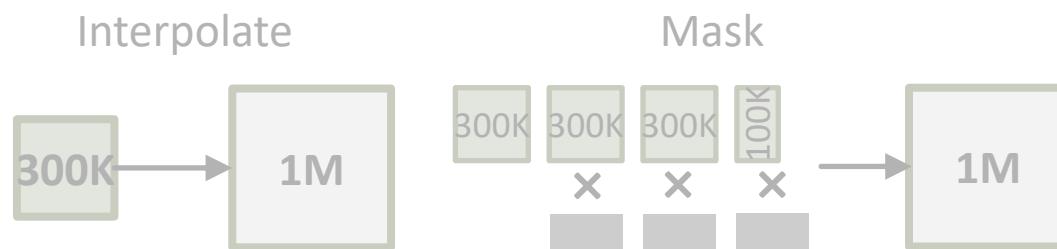
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



Weight Generation

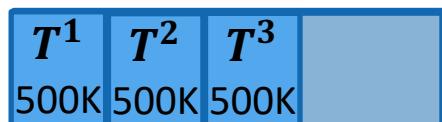
Upsample:



Downsample:

WAvg
[Savarese & Maire, 2019]

$K = 3$ templates

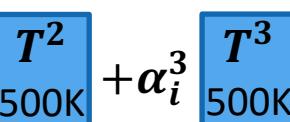


Coefficients α_i

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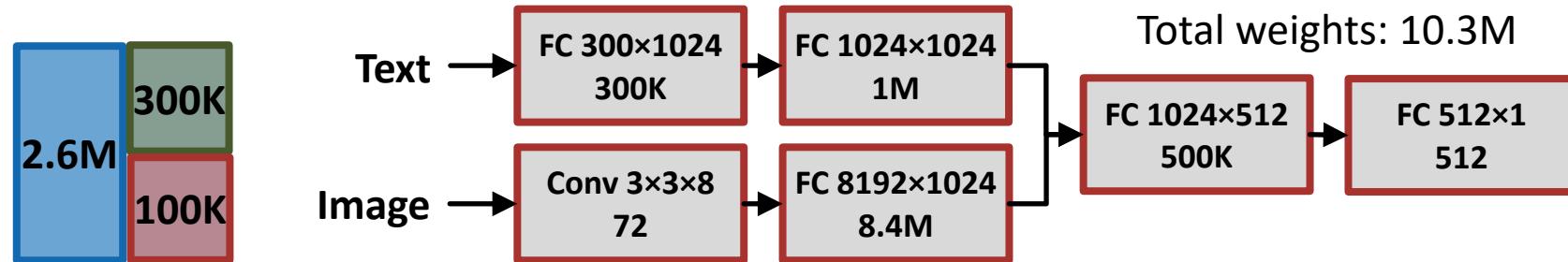
Embedding

Vector ϕ_i



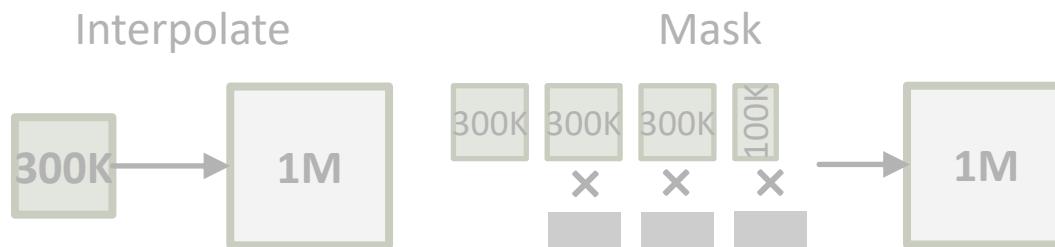
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



Weight Generation

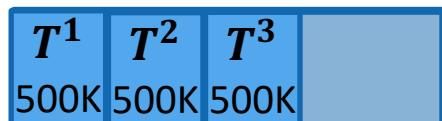
Upsample:



Downsample:

WAvg
 [Savarese & Maire, 2019]

$K = 3$ templates



Coefficients α_i

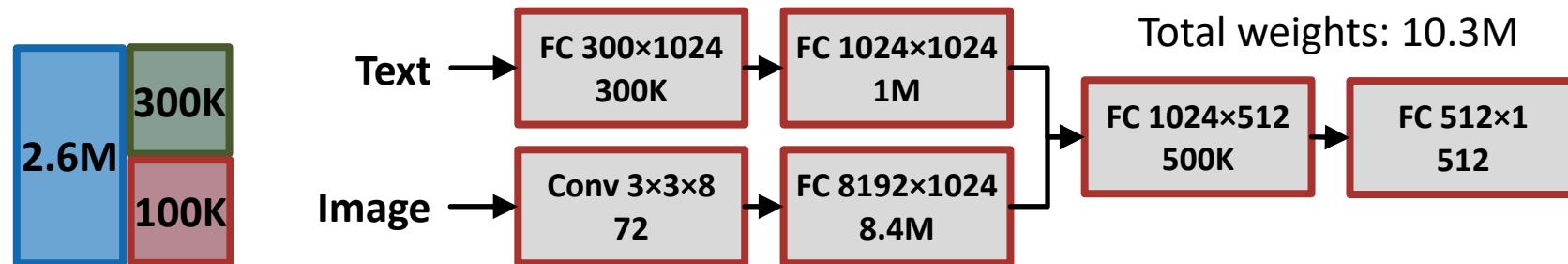
$$500K = \alpha_i^1 \begin{matrix} T^1 \\ 500K \end{matrix} + \alpha_i^2 \begin{matrix} T^2 \\ 500K \end{matrix} + \alpha_i^3 \begin{matrix} T^3 \\ 500K \end{matrix}$$

Embedding

Vector $\phi_i \longrightarrow \alpha_i = W\phi_i + b$

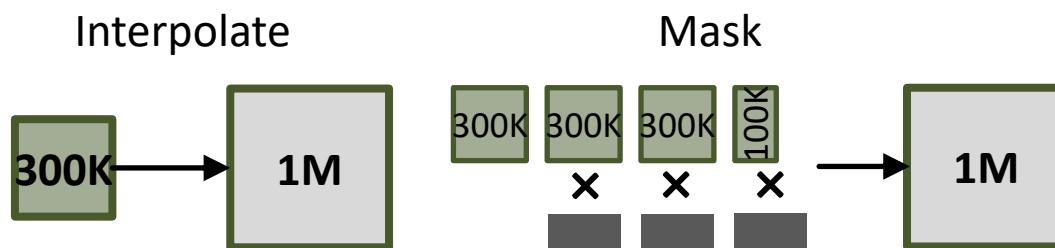
Shapeshifter Networks (SSNs)

Parameter budget: 3M
 $P = 3$ parameter groups



Weight Generation

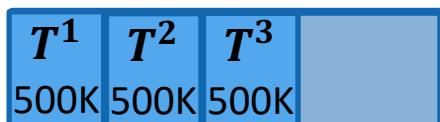
Upsample:



Downsample:

WAvg
 [Savarese & Maire, 2019]

$K = 3$ templates



Coefficients α_i

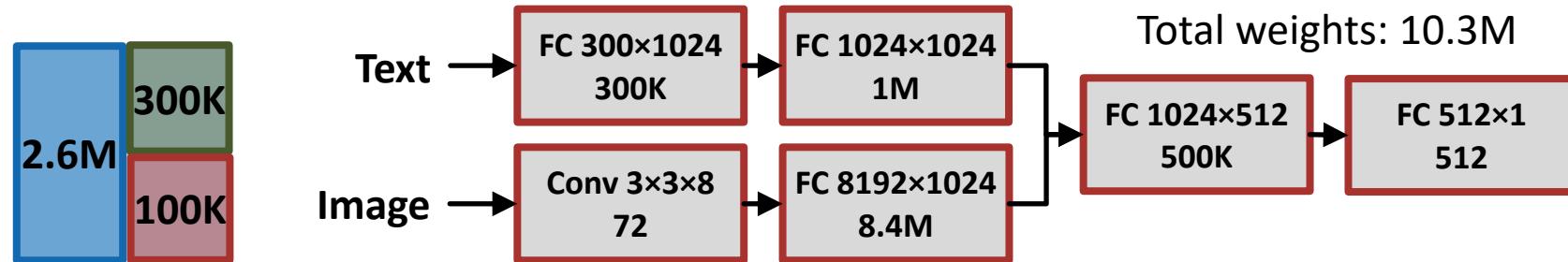
$$500K = \alpha_i^1 \begin{matrix} T^1 \\ 500K \end{matrix} + \alpha_i^2 \begin{matrix} T^2 \\ 500K \end{matrix} + \alpha_i^3 \begin{matrix} T^3 \\ 500K \end{matrix}$$

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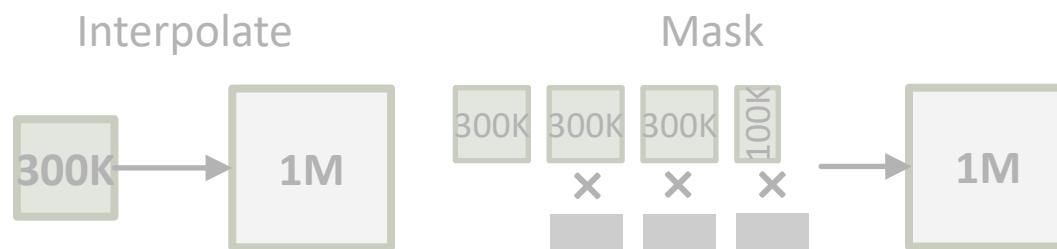
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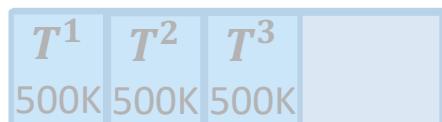
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Parameter Mapping

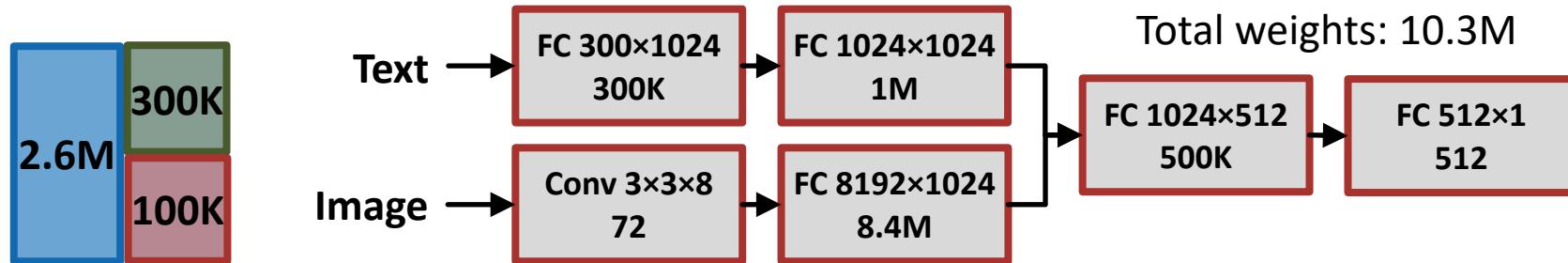
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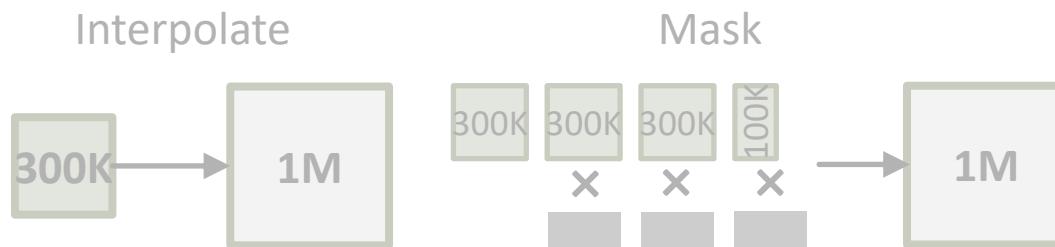
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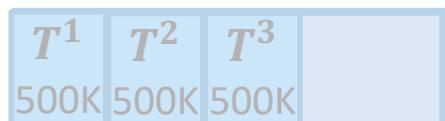
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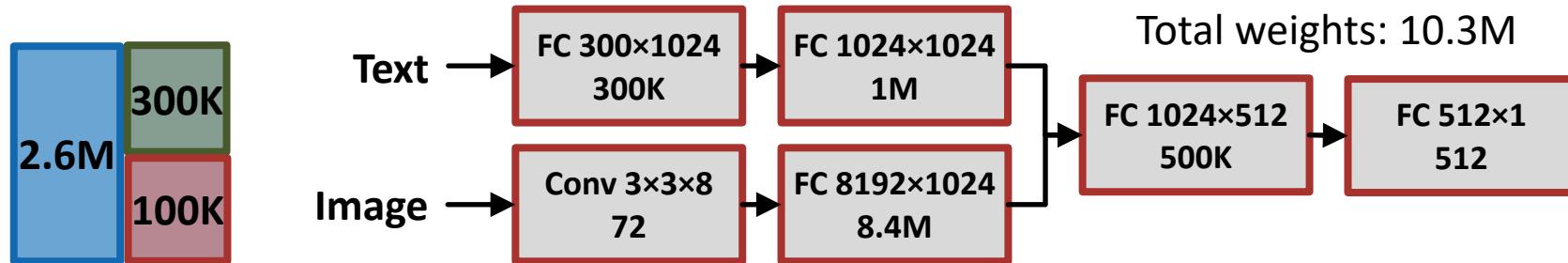
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Parameter Mapping

Learn layer representations

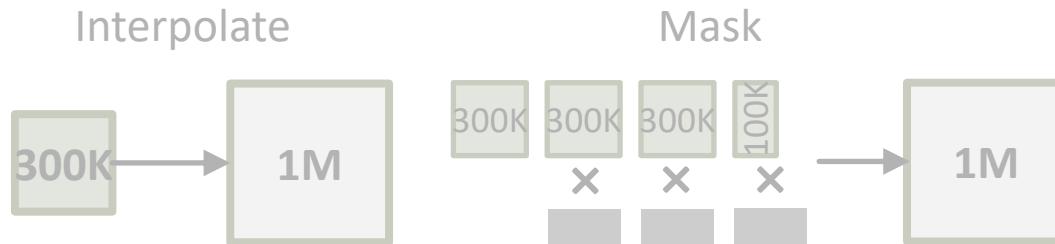
Shapeshifter Networks (SSNs)

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Weight Generation

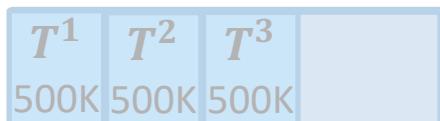
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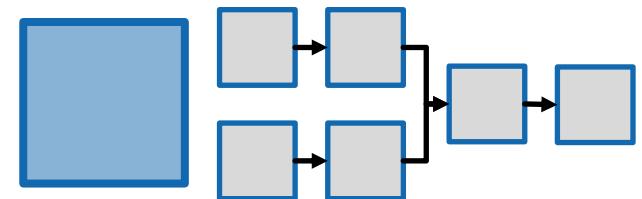
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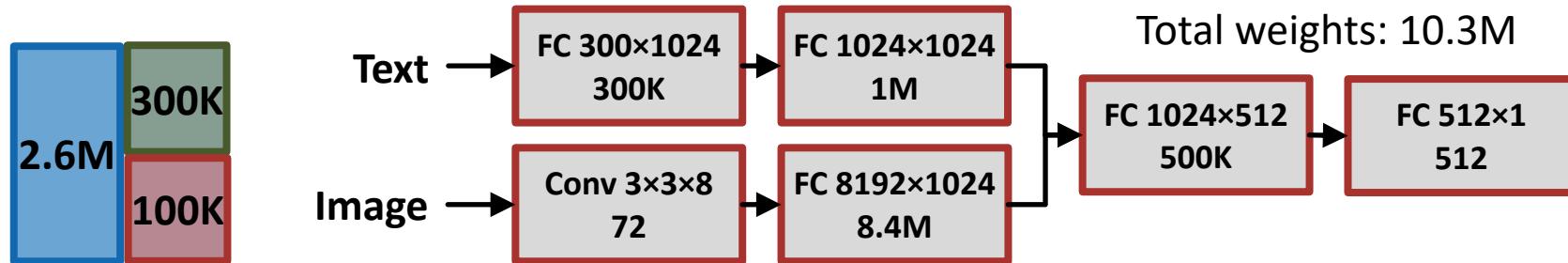
Parameter Mapping

Learn layer representations



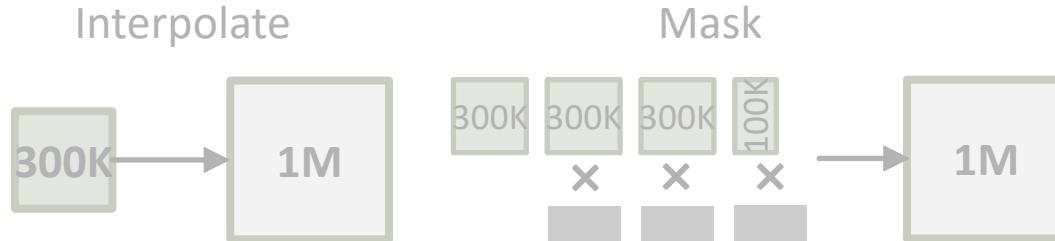
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Weight Generation

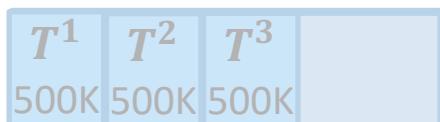
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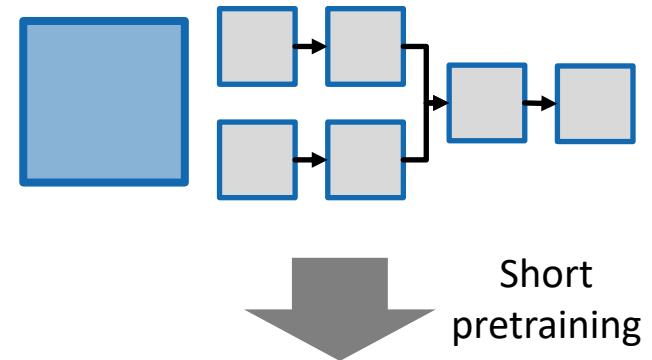
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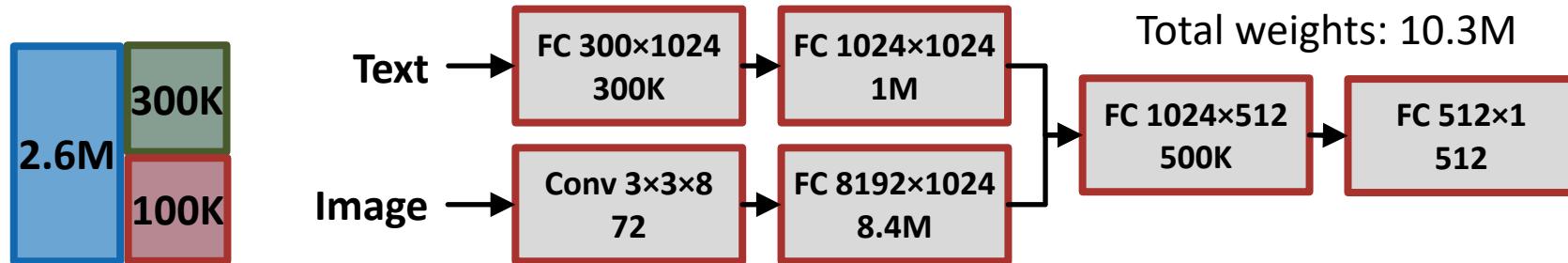
Parameter Mapping

Learn layer representations



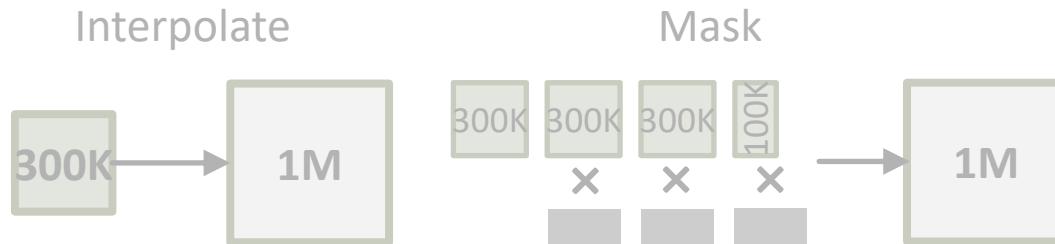
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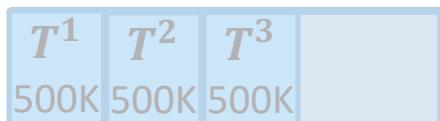
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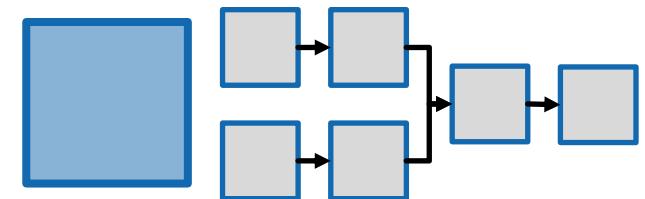
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Embedding

Vector $\phi_i \rightarrow \alpha_i = W\phi_i + b$

Parameter Mapping

Learn layer representations



Short
 pretraining

Cluster layer representations

Performance: Question Answering

Performance: Question Answering

	Model	#Params	SQuAD v1.1	SQuAD v2.0
Base	BERT	108M	90.4 / 83.2	80.4 / 77.6
	BERT	334M	92.2 / 85.5	85.0 / 82.2

Performance: Question Answering

	Model	#Params	SQuAD v1.1	SQuAD v2.0
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1.4× faster training speed compared to BERT-Large on 128 V100 GPUs

Performance: Question Answering

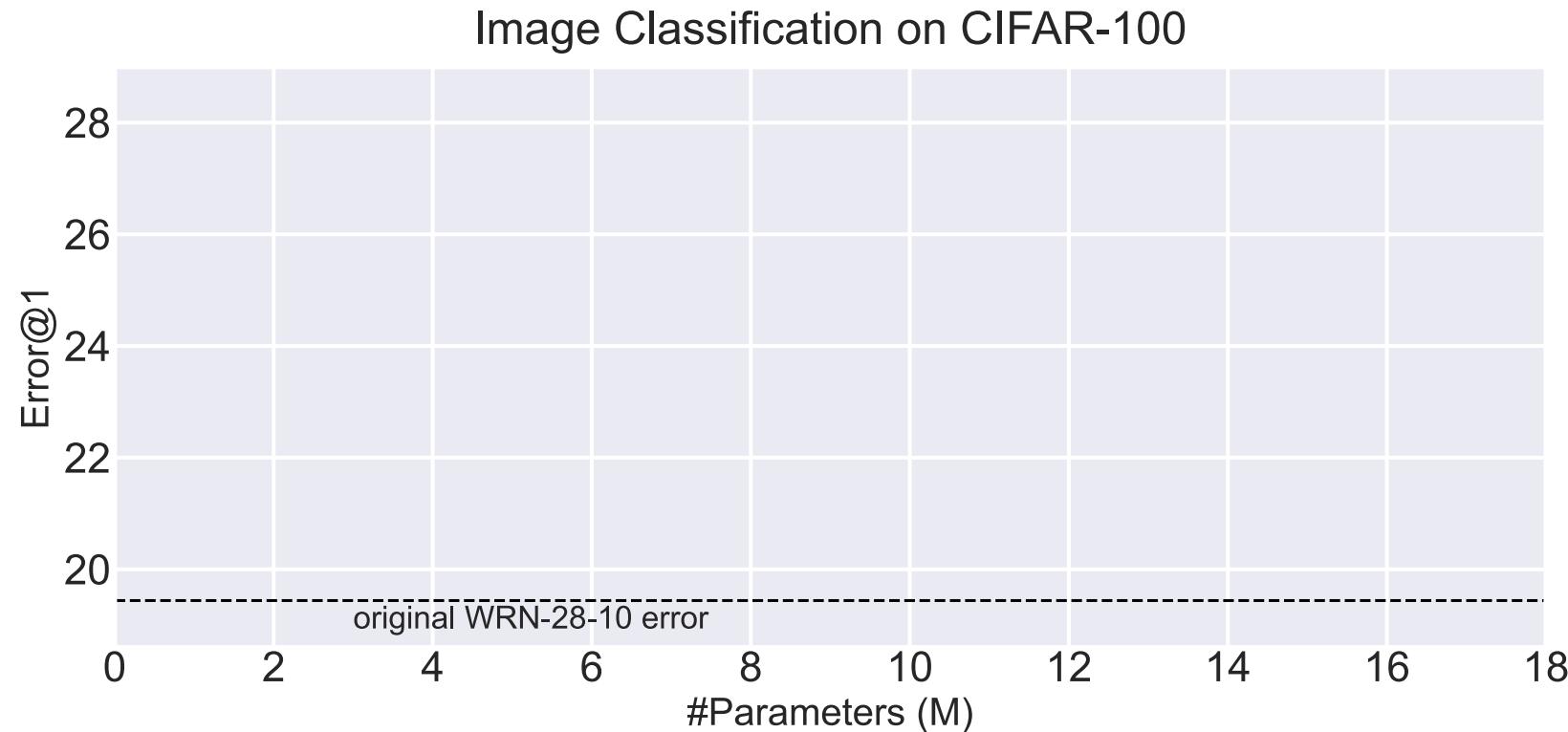
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**1.4× fastless memory usage
compared to BERT-Large on 128 V100 GPUs**

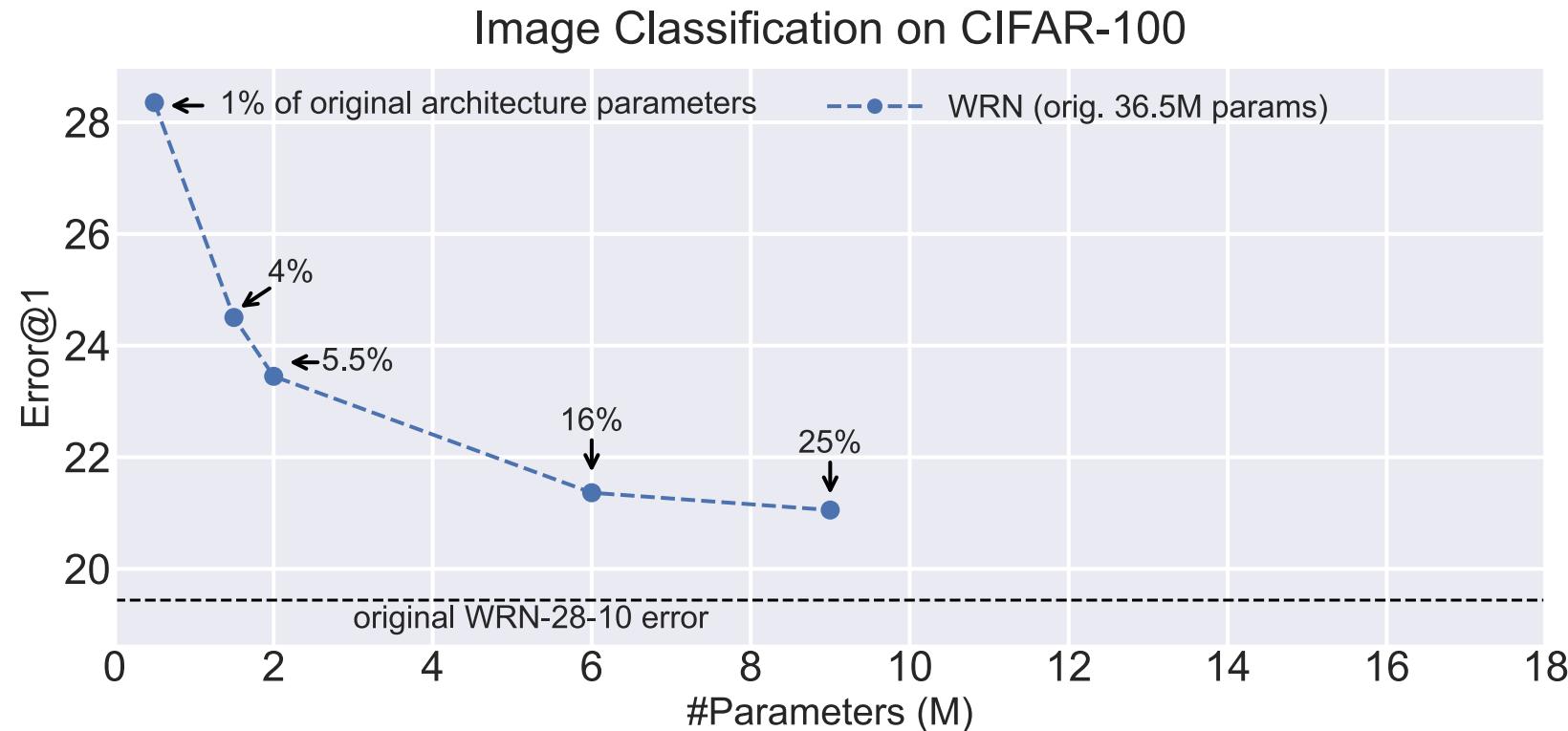
1 3 3 3 3 less memory usage

Performance: Image Classification

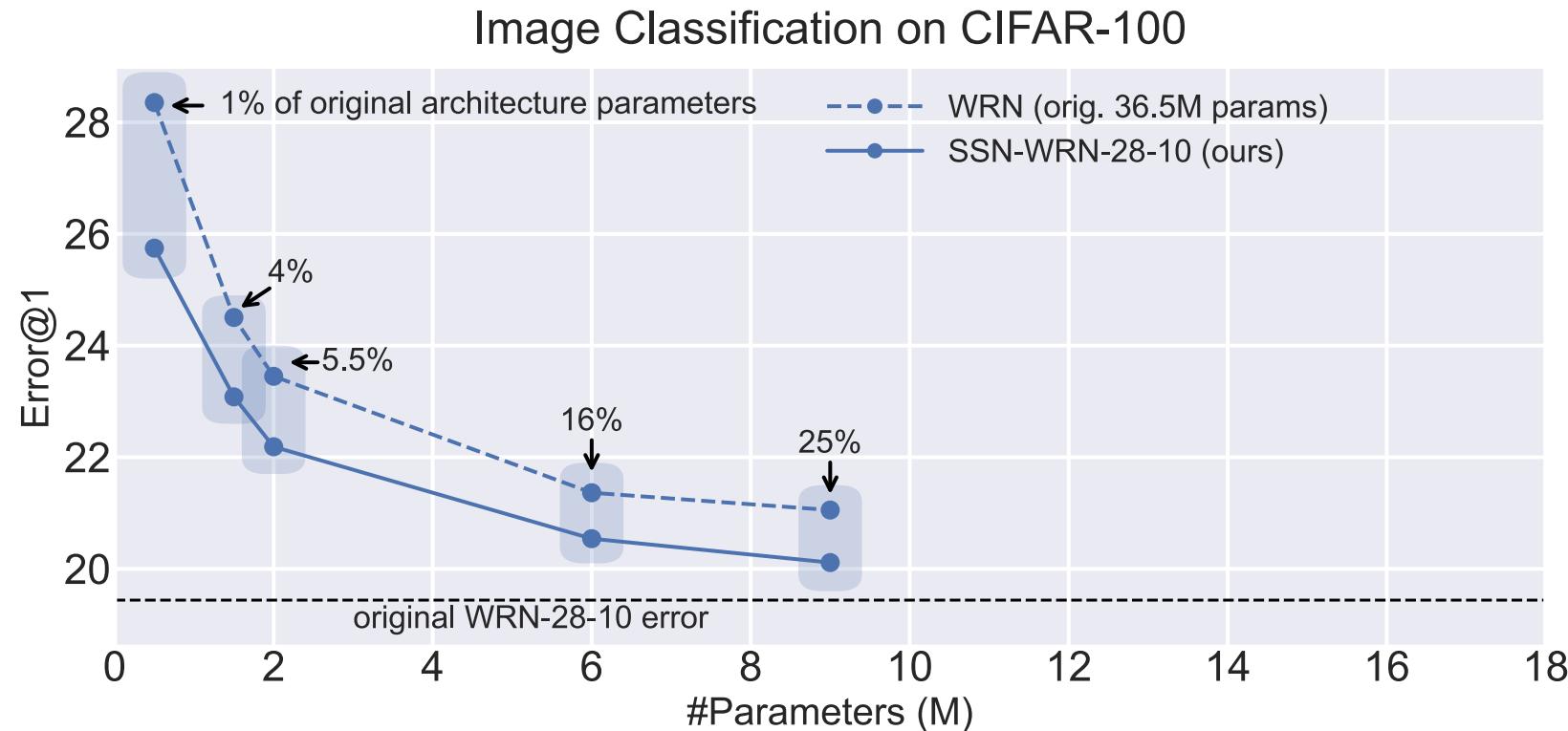
Performance: Image Classification



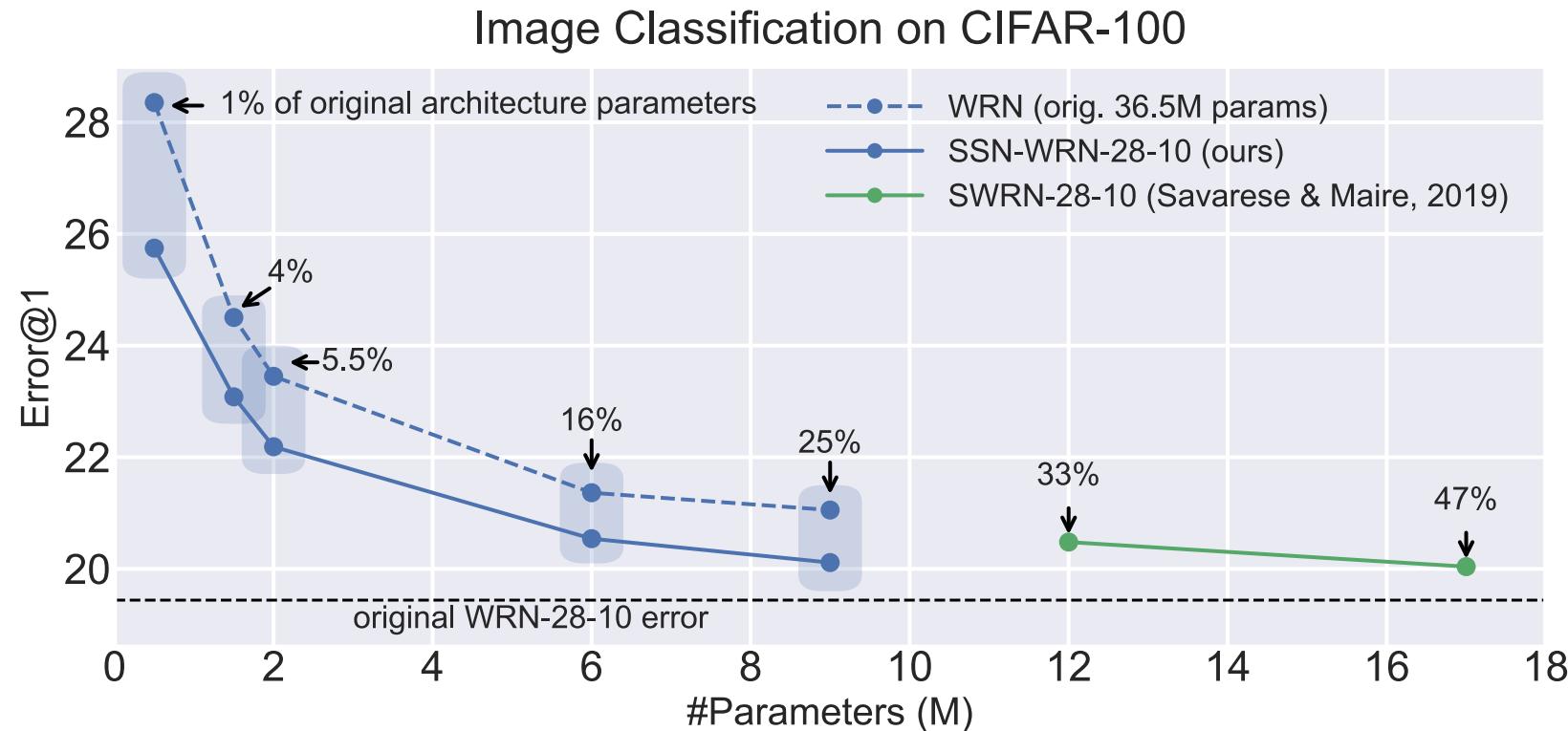
Performance: Image Classification



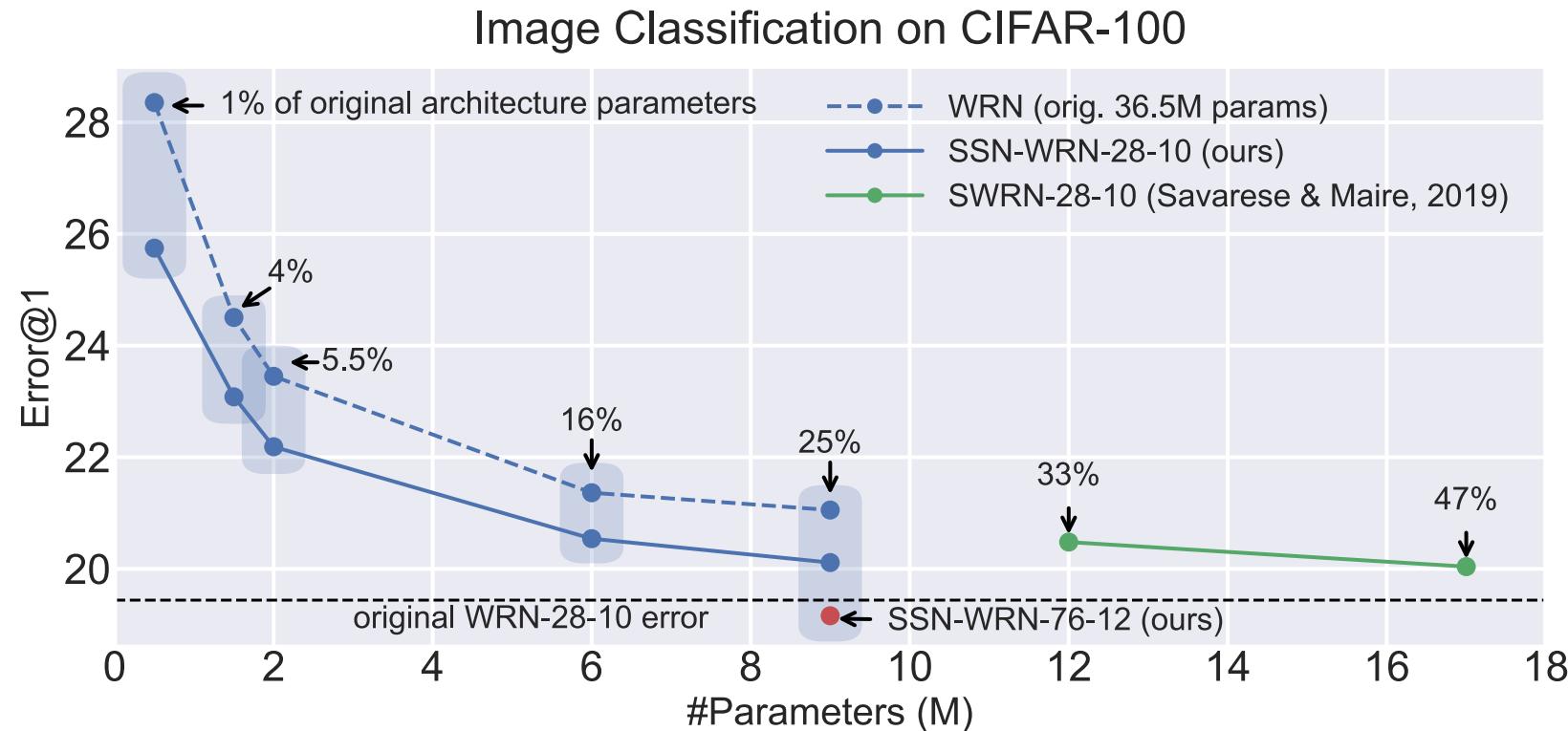
Performance: Image Classification



Performance: Image Classification

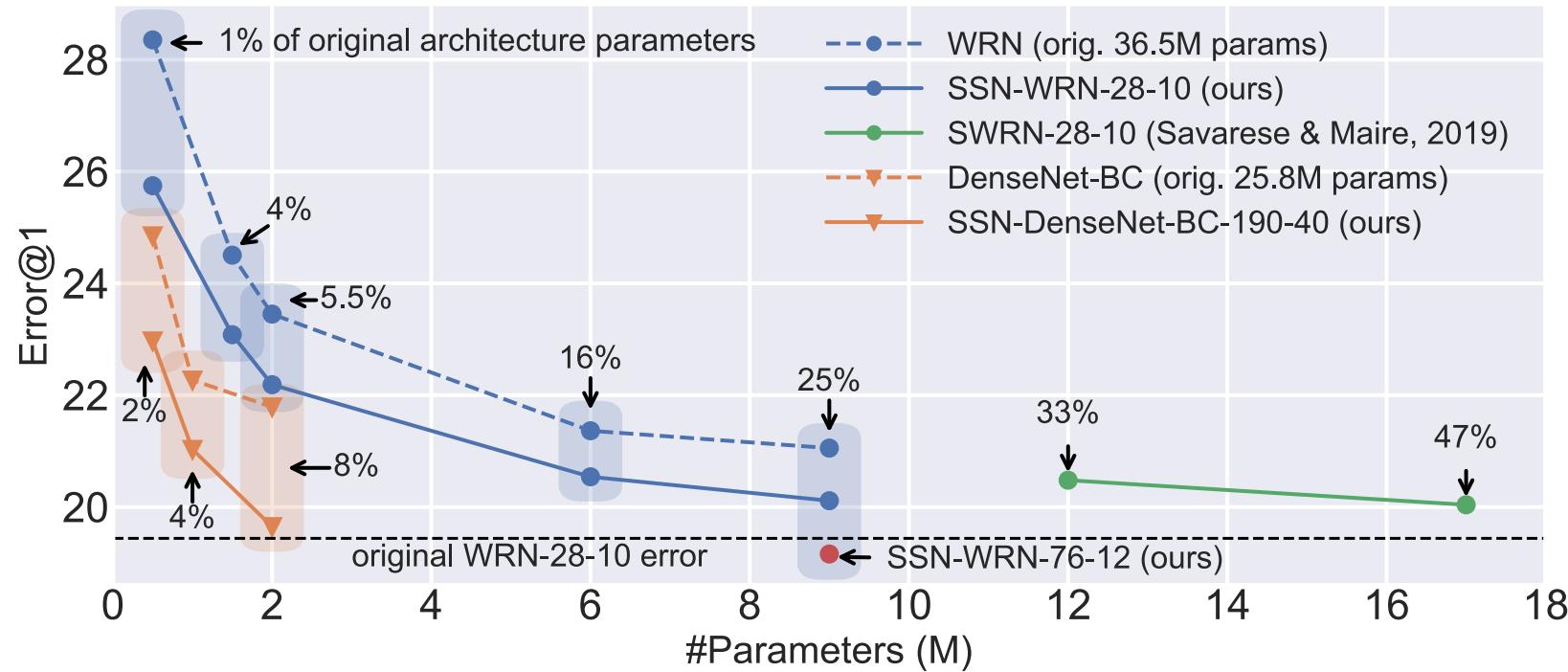


Performance: Image Classification



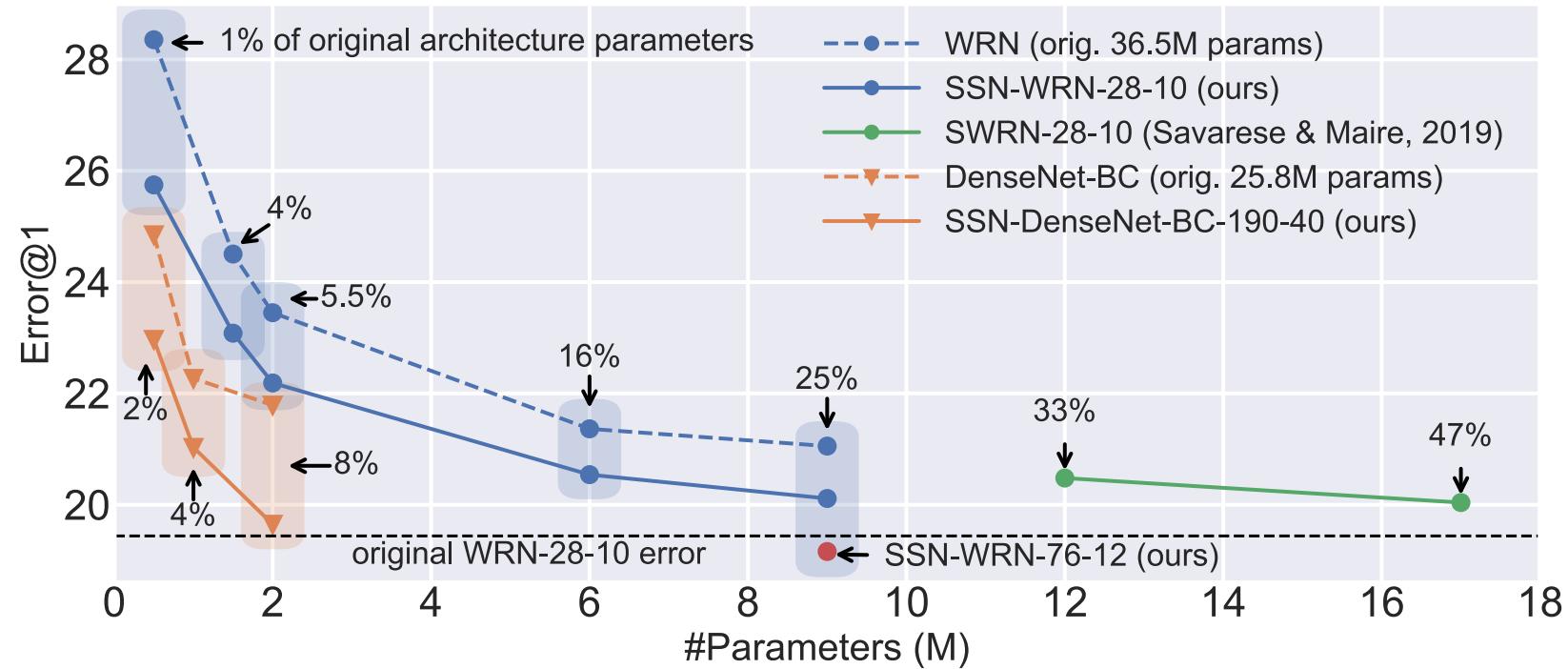
Performance: Image Classification

Image Classification on CIFAR-100

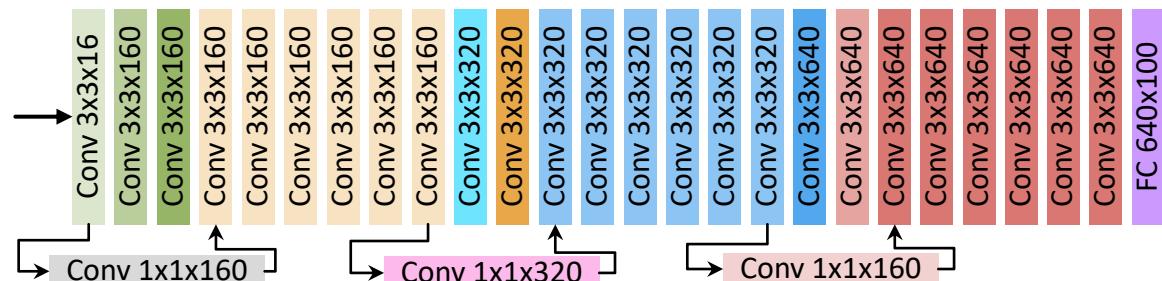


Performance: Image Classification

Image Classification on CIFAR-100

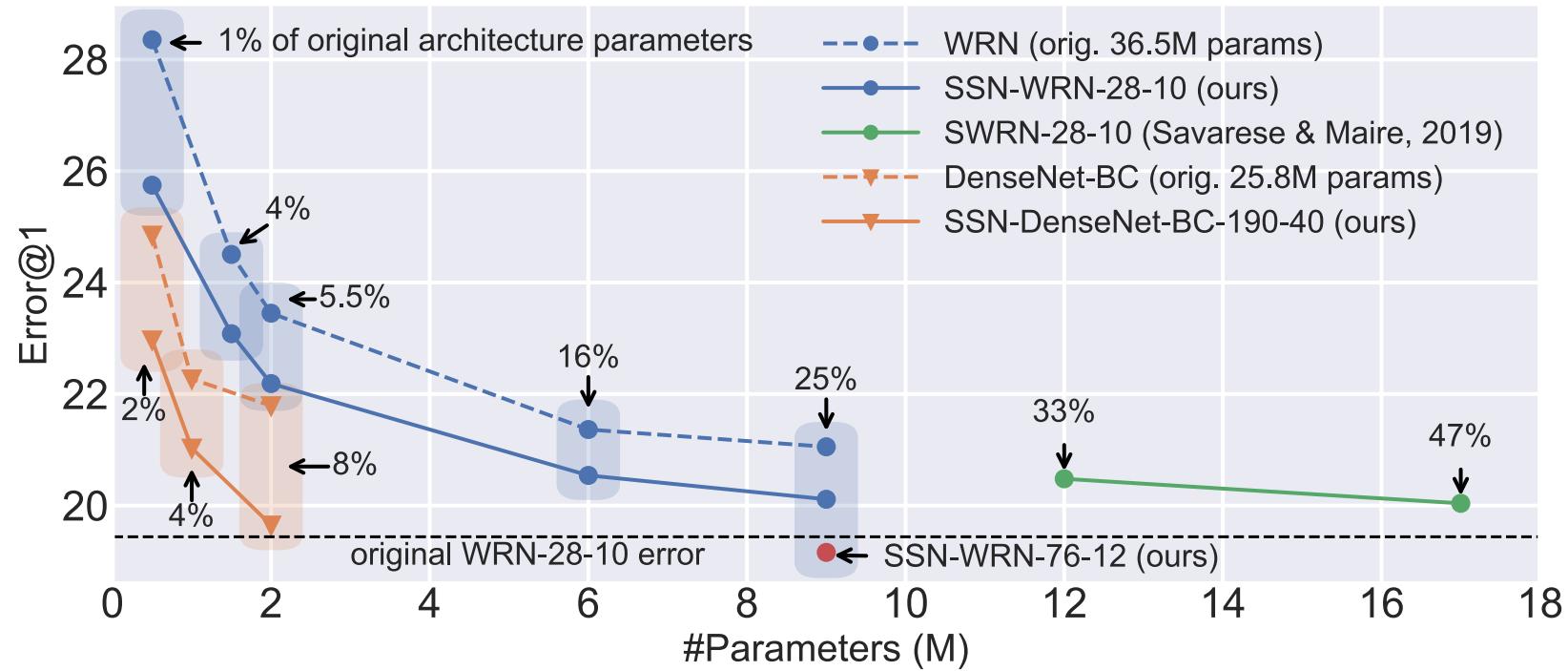


Manual (Savarese & Maire, 2019)

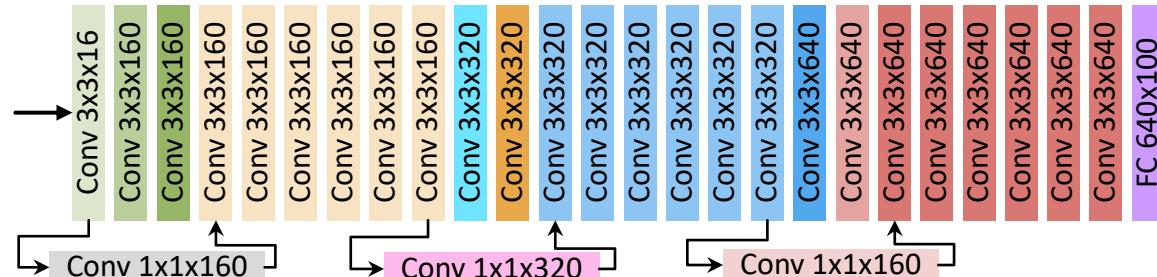


Performance: Image Classification

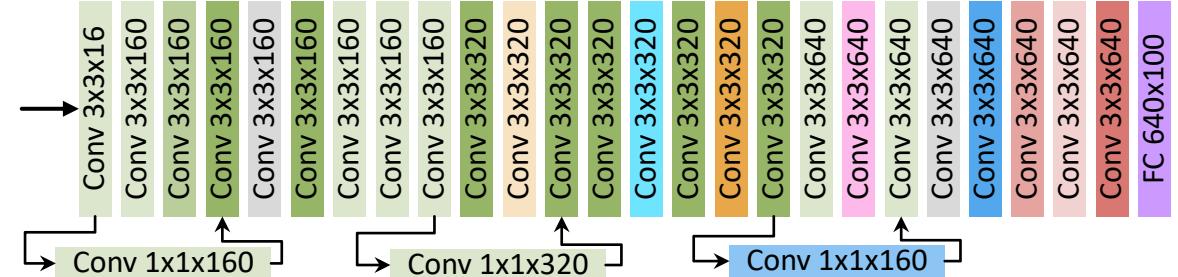
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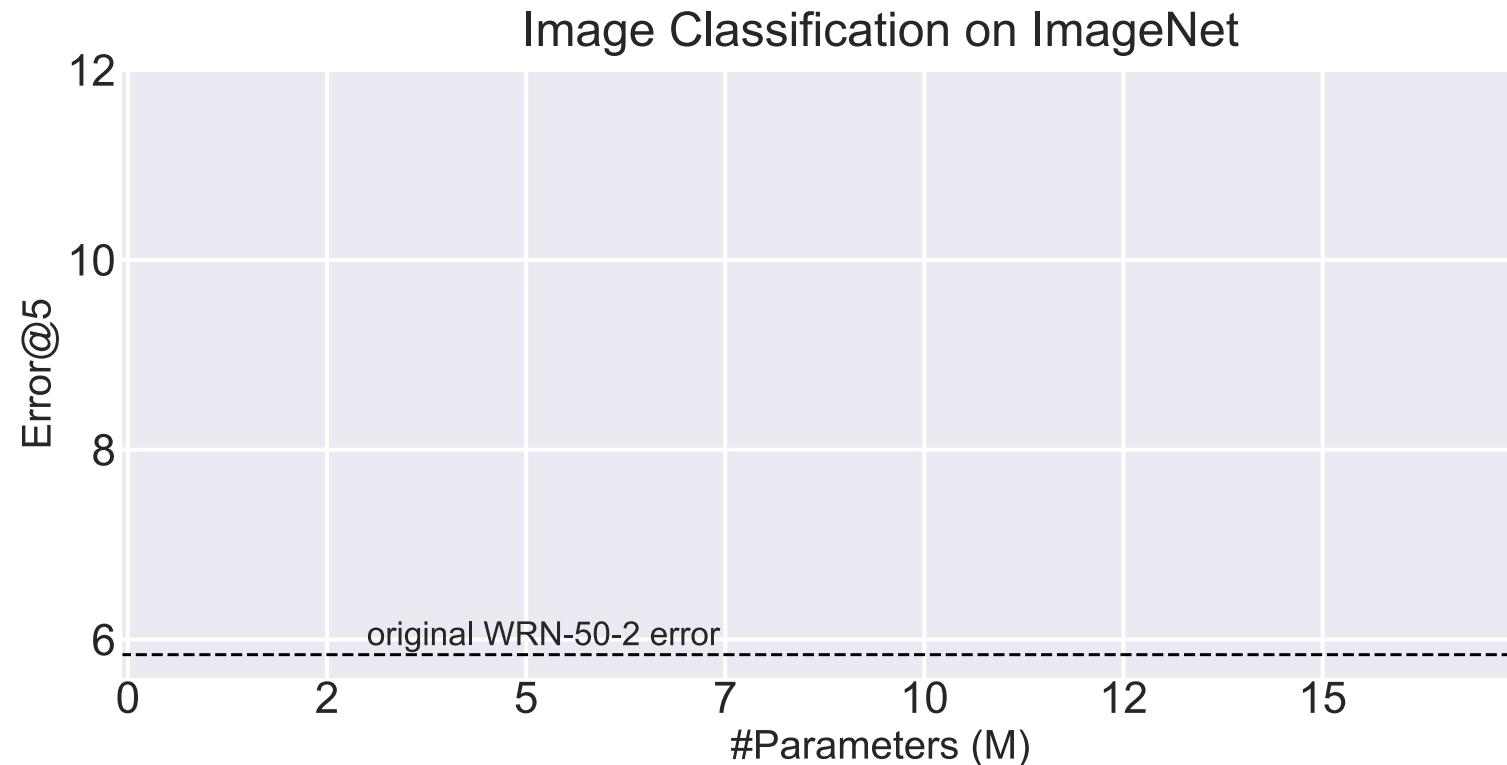
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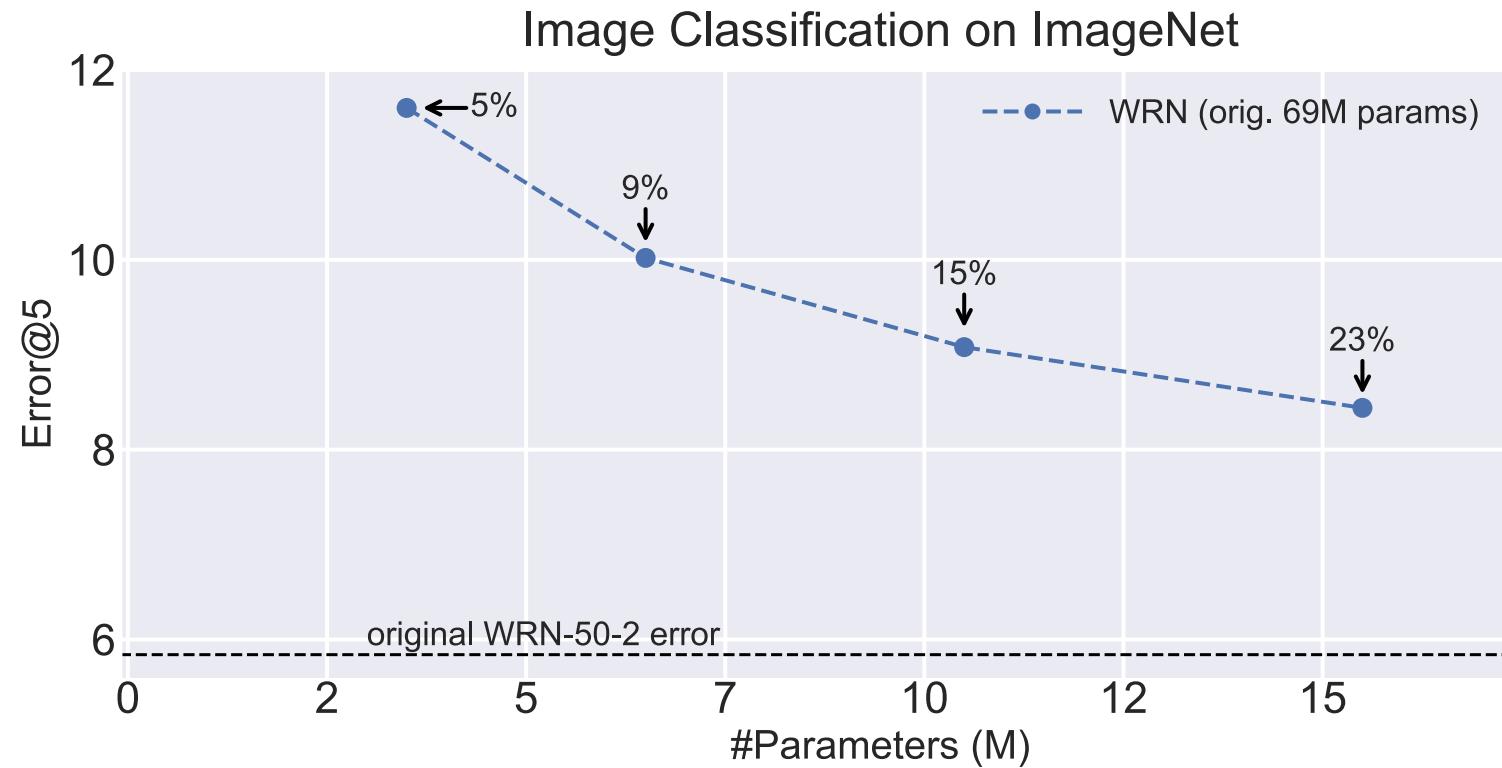
Learned (ours)



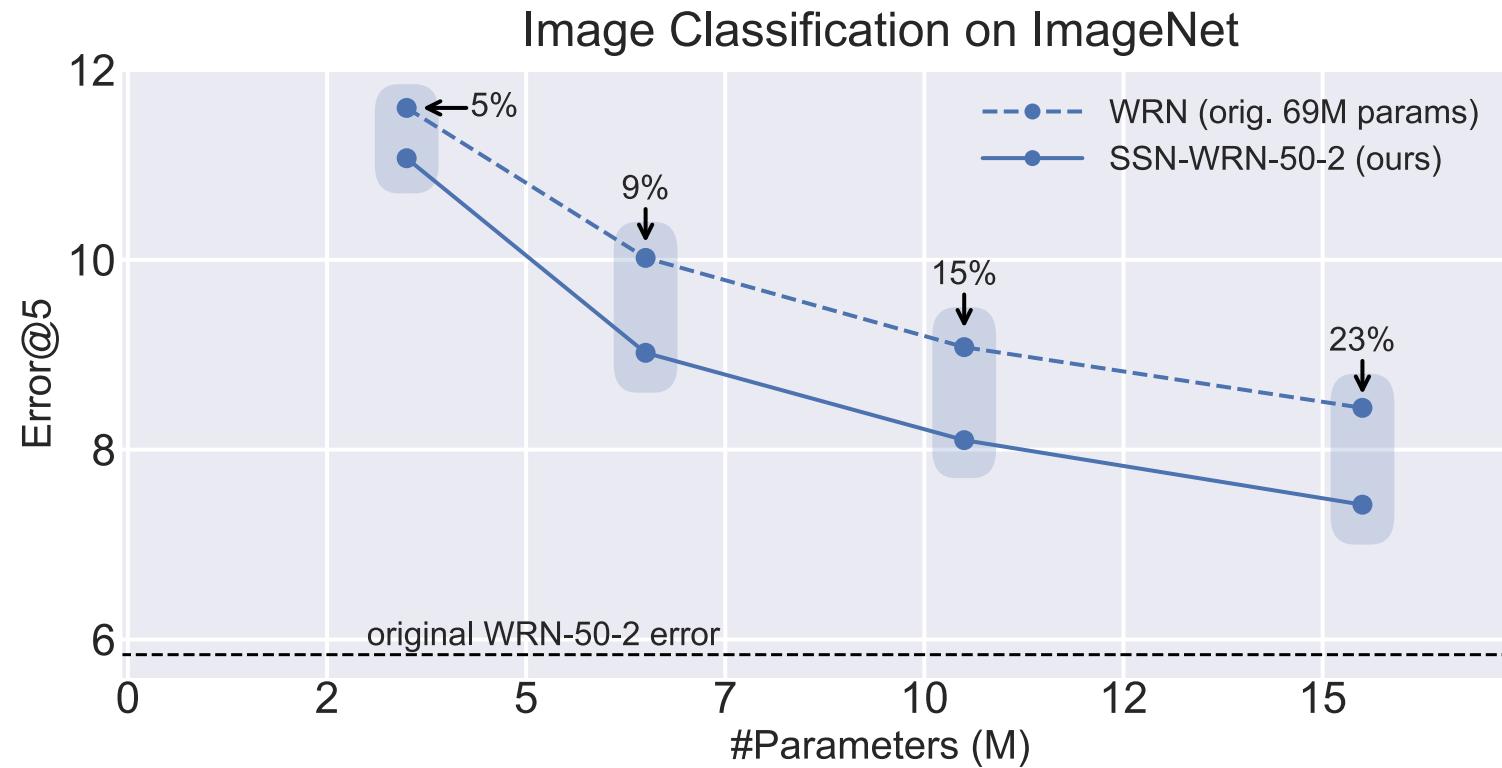
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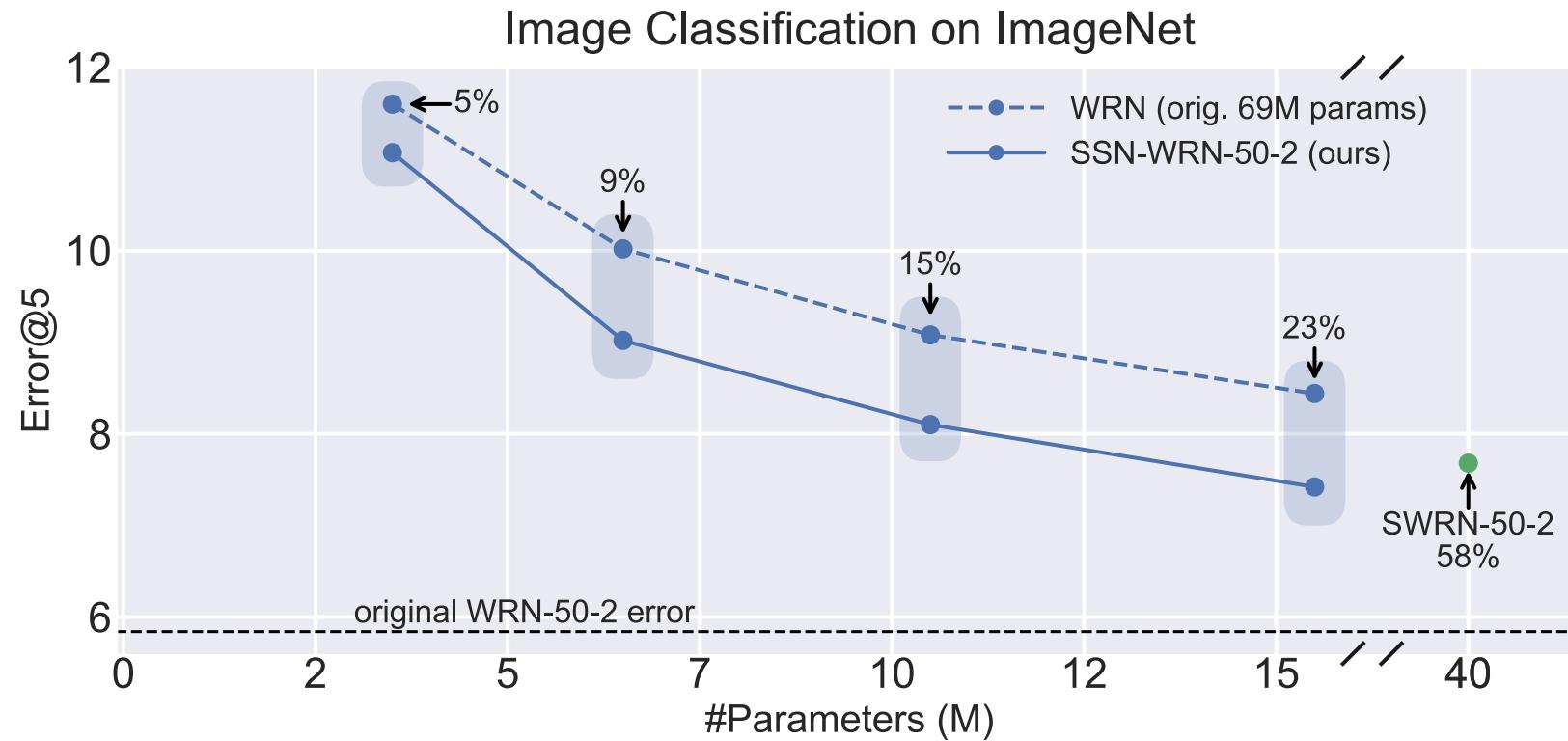
Performance: Image Classification



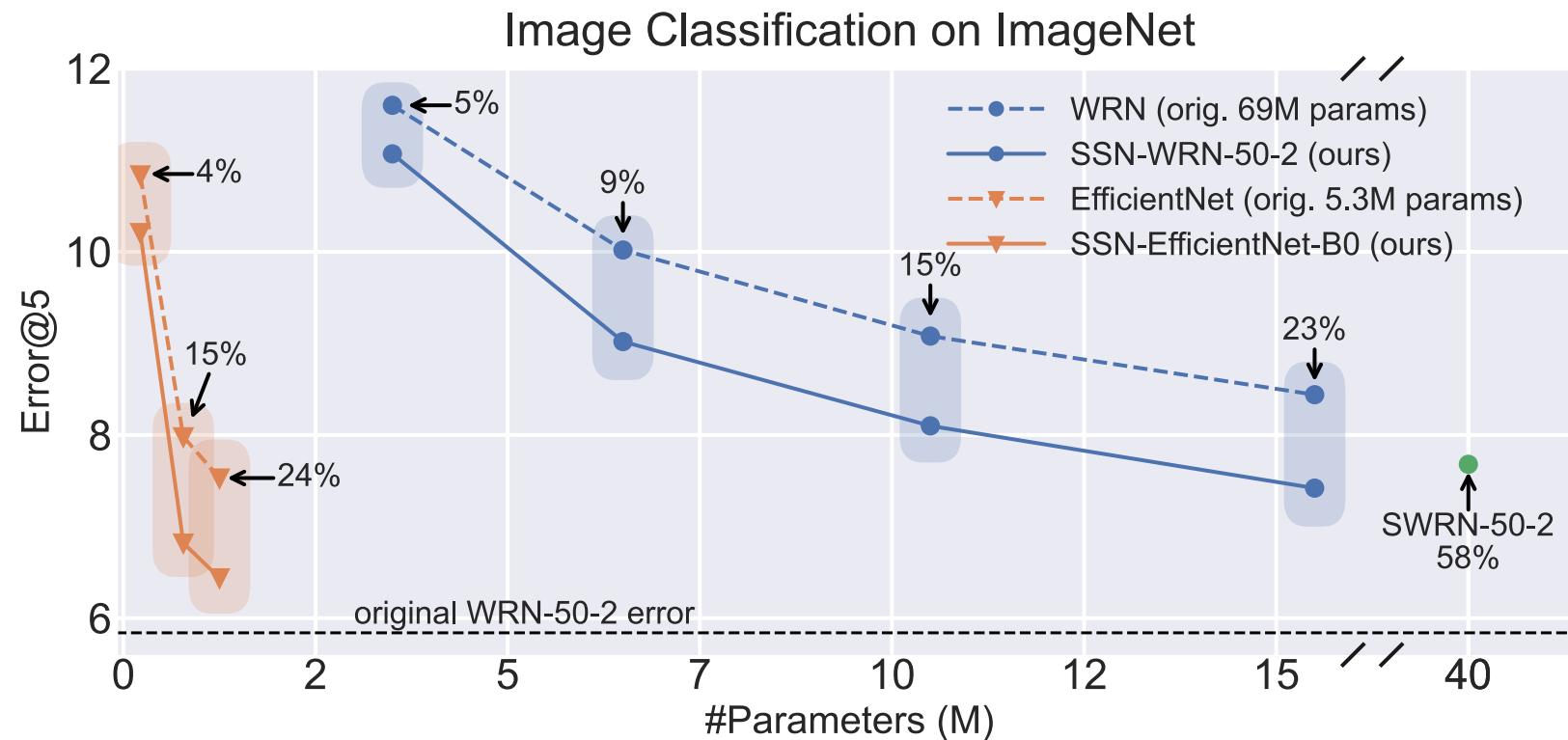
Performance: Image Classification



Performance: Image Classification



Performance: Image Classification



Pruning

Pruning

Pruning
CIFAR-10

Method	Error@1	% original inference flop	% original params
	ResNet-56		
Base	6.74	100.0	100.0

Pruning

Pruning
CIFAR-10

Method	Error@1		% original inference flop	% original params
	ResNet-56	HB(4×)-SSN		
Base	6.74	5.21	100.0	100.0

Pruning

Pruning
CIFAR-10

Method	Error@1		% original inference flop	% original params
	ResNet-56	HB(4×)-SSN		
Base	6.74	5.21	100.0	100.0
HRank	6.48	5.86	70.7	83.2
HRank	10.75	9.94	20.3	28.4

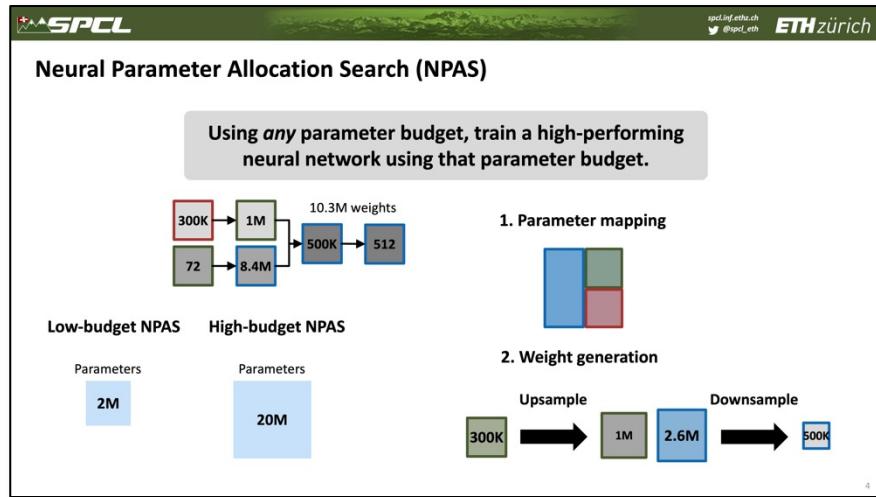
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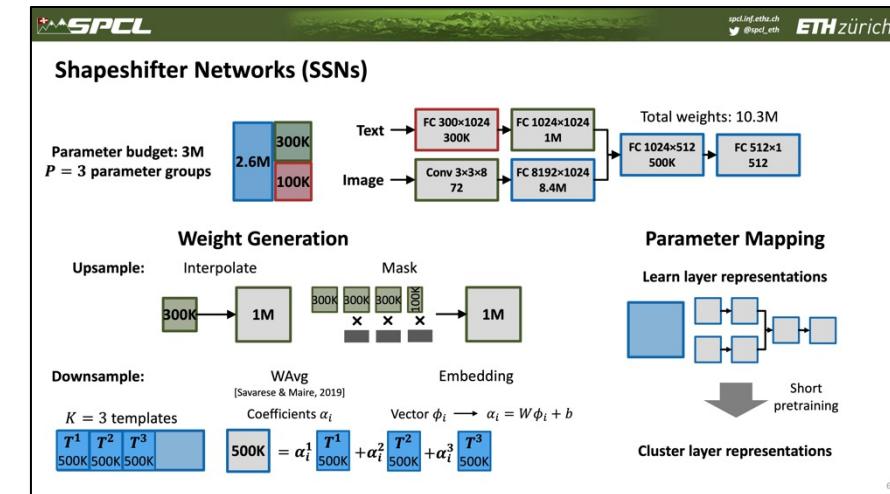
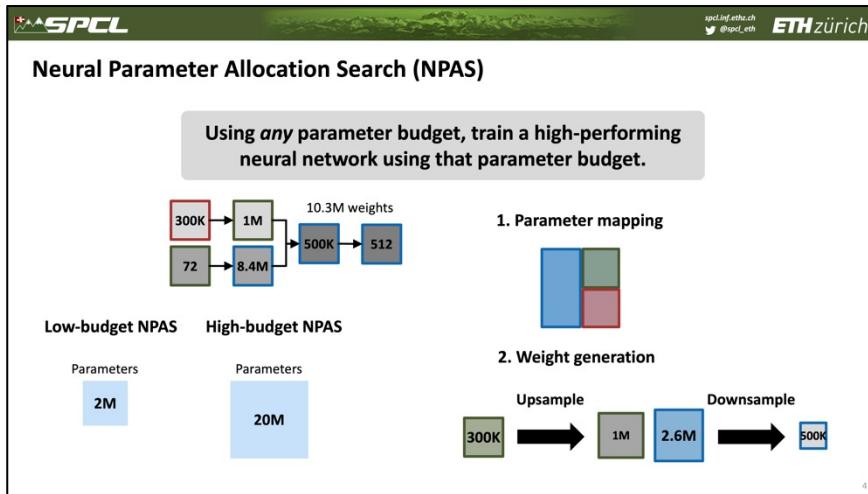
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Conclusions

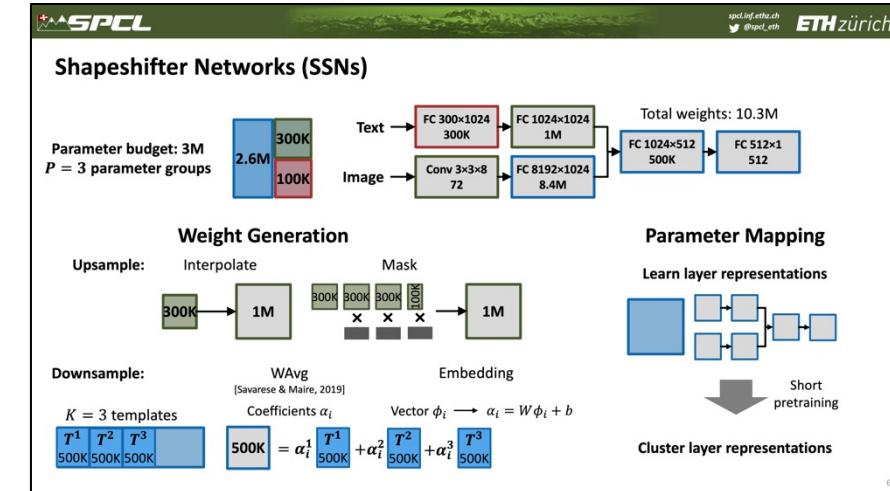
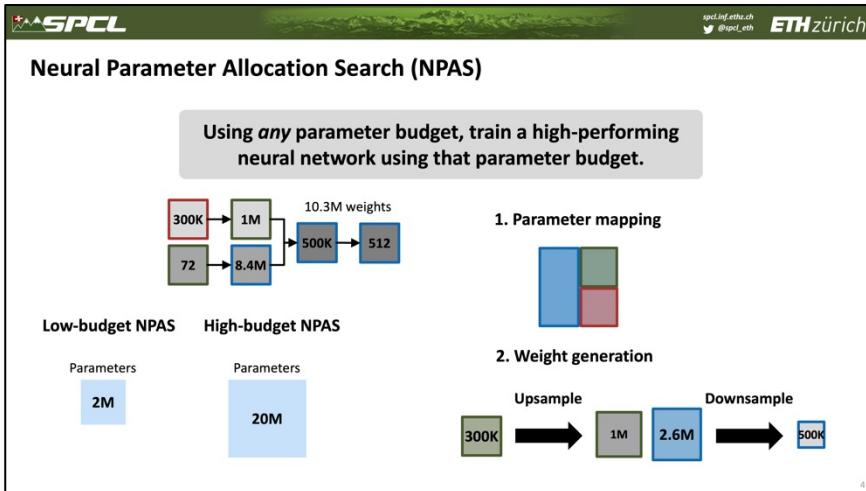
Conclusions



Conclusions



Conclusions



Performance: Question Answering

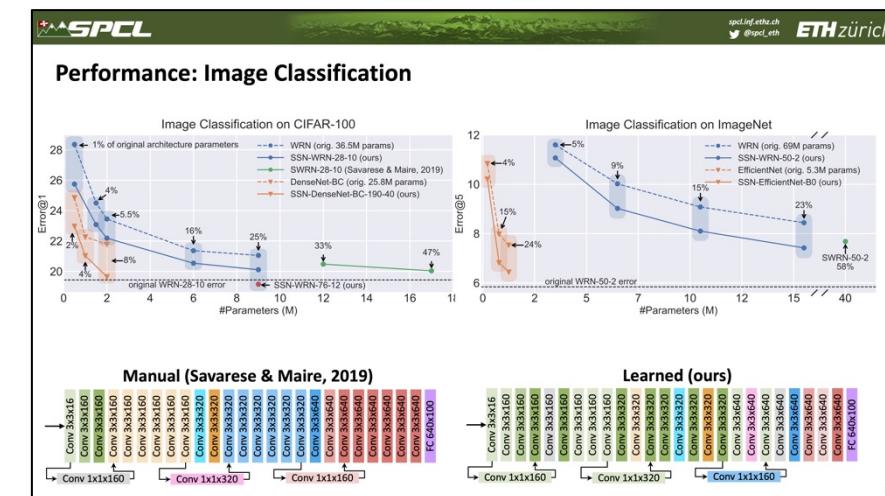
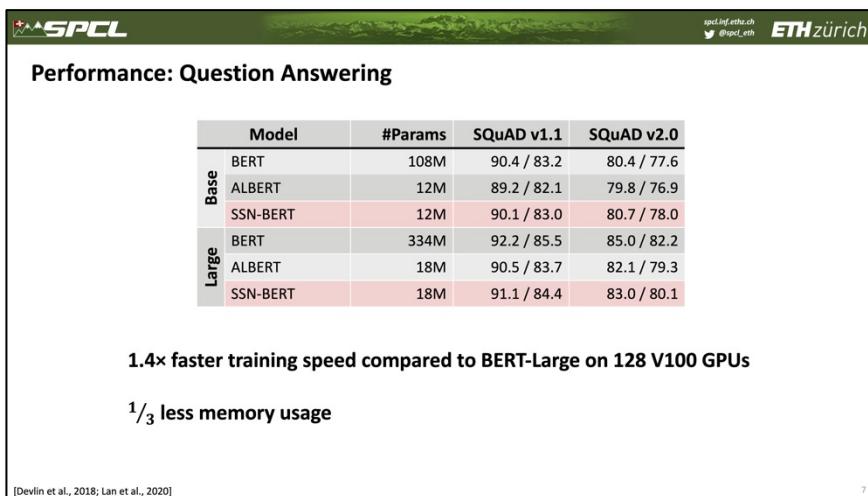
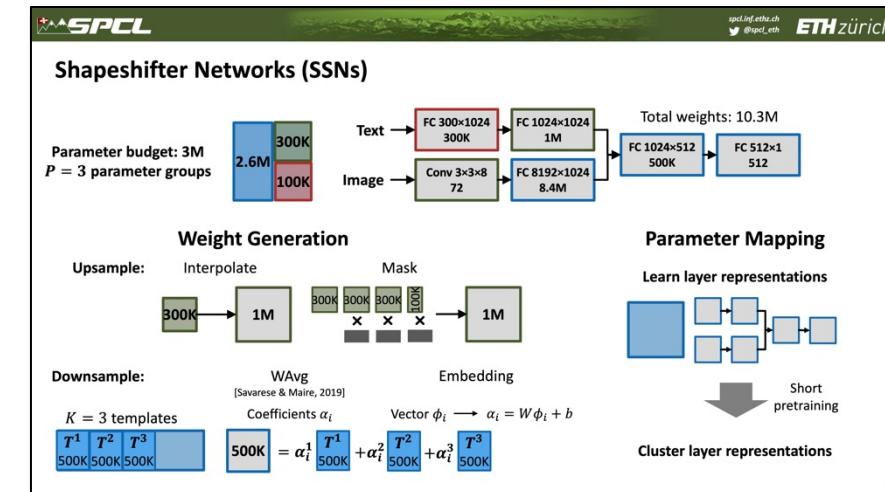
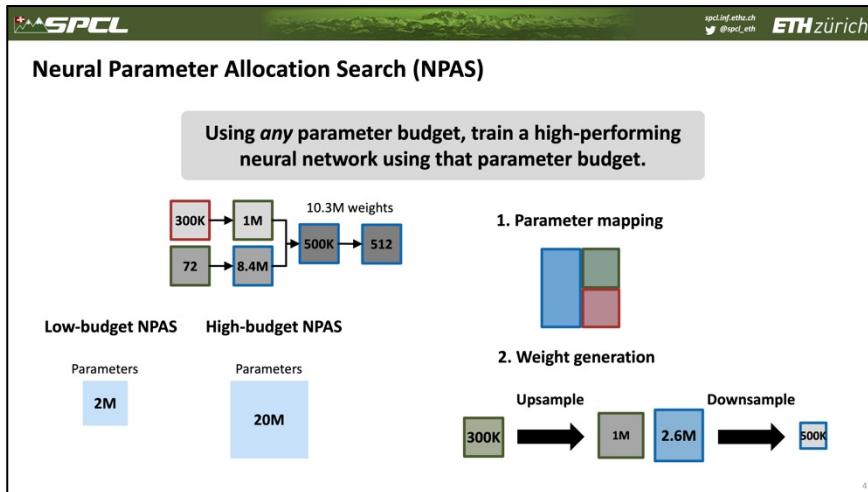
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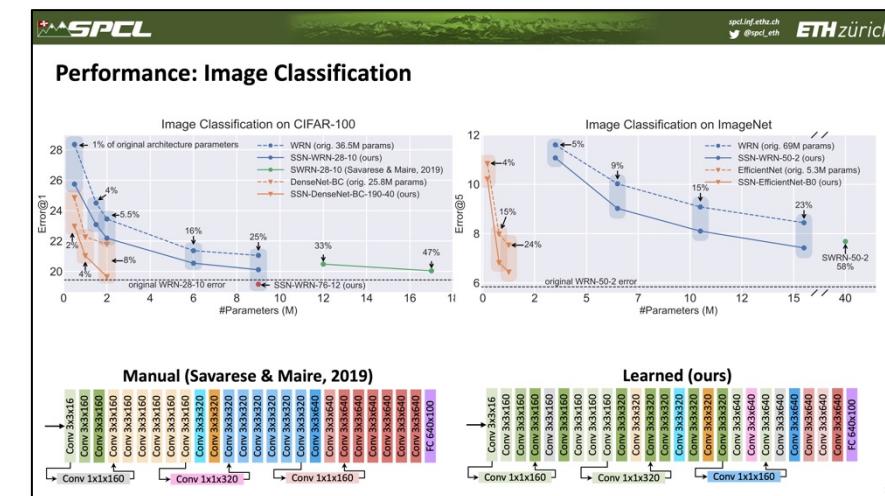
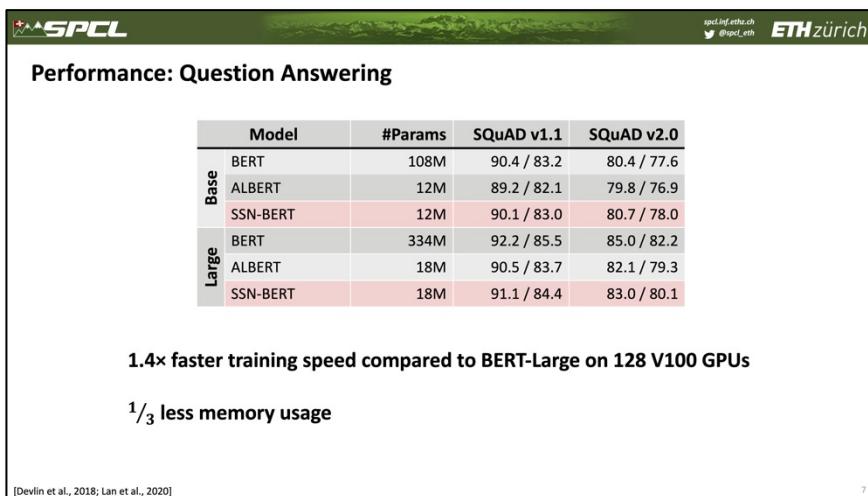
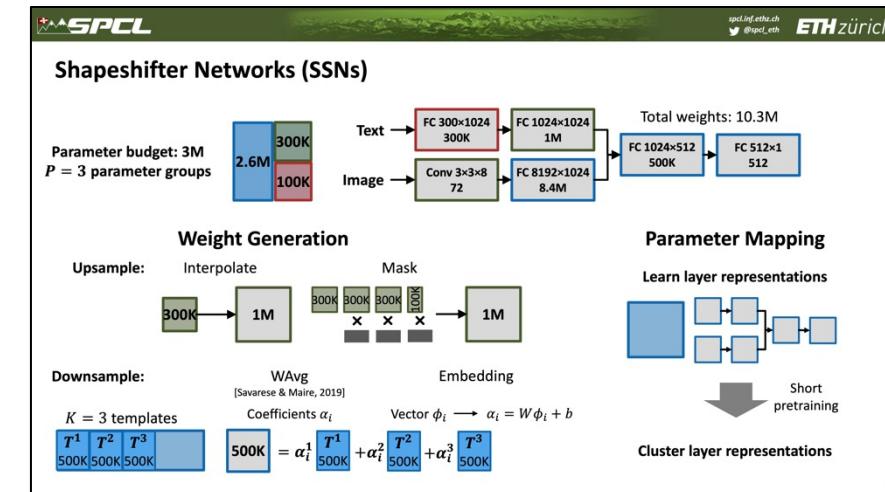
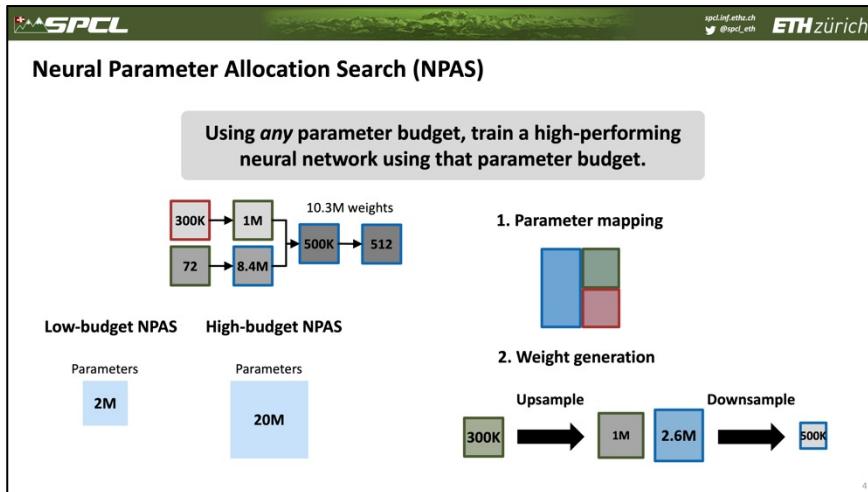
1/3 less memory usage

[Devlin et al., 2018; Lan et al., 2020]

Conclusions



Conclusions



<https://github.com/BryanPlummer/SSN>